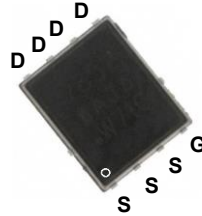
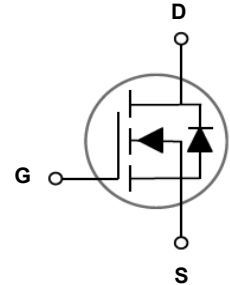


Main Product Characteristics

$V_{(BR)DSS}$	65V
$R_{DS(ON)}$	4.2m Ω
I_D	90A



PPAK5X6



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The SSFP6976 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supply and a wide variety of other applications.

Absolute Maximum Ratings (T_c=25°C unless otherwise specified)

Parameter	Symbol	Rating	Unit
Drain-Source Voltage	V _{DS}	65	V
Gate-Source Voltage	V _{GS}	+20/-12	V
Drain Current – Continuous (T _c =25°C)	I _D	90	A
Drain Current – Continuous (T _c =100°C)		57	A
Drain Current – Pulsed ¹	I _{DM}	360	A
Single Pulse Avalanche Energy ²	E _{AS}	174	mJ
Single Pulse Avalanche Current ²	I _{AS}	59	A
Power Dissipation (T _c =25°C)	P _D	125	W
Power Dissipation – Derate above 25°C		1	W/°C
Storage Temperature Range	T _{STG}	-50 to +150	°C
Operating Junction Temperature Range	T _J	-50 to +150	°C

Thermal Characteristics

Parameter	Symbol	Typ.	Max.	Unit
Thermal Resistance Junction to Ambient	R _{θJA}	---	62	°C/W
Thermal Resistance Junction to Case	R _{θJC}	---	1	°C/W

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Test Conditions	Typical	Max	Min	Unit
DC Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	65	---	---	V
BV_{DSS} Temperature Coefficient	$\Delta BV_{DSS}/\Delta T_J$	Reference to $25^\circ\text{C}, I_D=1mA$	---	0.02	---	$V/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{DS}=60V, V_{GS}=0V, T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{DS}=48V, V_{GS}=0V, T_J=85^\circ\text{C}$	---	---	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{GS}=20V, V_{DS}=0V$	---	---	100	nA
Static Characteristics						
Static Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS}=10V, I_D=15A$	---	3.4	4.2	m Ω
		$V_{GS}=4.5V, I_D=8A$	---	5.4	7.2	m Ω
Gate Threshold Voltage	$V_{GS(th)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1	1.5	2.5	V
$V_{GS(th)}$ Temperature Coefficient	$\Delta V_{GS(th)}$		---	-5.1	---	mV/ $^\circ\text{C}$
Forward Transconductance	g_{fs}	$V_{DS}=10V, I_D=3A$	---	12	---	S
Switching Characteristics						
Total Gate Charge ^{3, 4}	Q_g	$V_{DS}=48V, V_{GS}=10V, I_D=5A$	---	54	108	nC
Gate-Source Charge ^{3, 4}	Q_{gs}		---	5.2	10.4	
Gate-Drain Charge ^{3, 4}	Q_{gd}		---	16.1	32.2	
Turn-On Delay Time ^{3, 4}	$T_{d(on)}$	$V_{DD}=30V, V_{GS}=10V, R_G=6\Omega, I_D=1A$	---	10.6	21	nS
Rise Time ^{3, 4}	T_r		---	16.5	33	
Turn-Off Delay Time ^{3, 4}	$T_{d(off)}$		---	48	96	
Fall Time ^{3, 4}	T_f		---	78	150	
Input Capacitance	C_{iss}	$V_{DS}=25V, V_{GS}=0V, F=1MHz$	---	2976	5800	pF
Output Capacitance	C_{oss}		---	950	190	
Reverse Transfer Capacitance	C_{rss}		---	24	48	
Gate Resistance	R_g	$V_{GS}=0V, V_{DS}=0V, F=1MHz$	---	1.24	---	Ω
Thermal Characteristics						
Continuous Source Current	I_S	$V_{GS}=V_{DS}=0V, \text{Force Current}$	---	---	90	A
Pulsed Source Current	I_{SM}		---	---	360	A
Diode Forward Voltage	V_{SD}	$V_{GS}=0V, I_S=1A, T_J=25^\circ\text{C}$	---	---	1	V

Note:

1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=59A, R_G=25\Omega, \text{Starting } T_J=25^\circ\text{C}.$
3. The data tested by pulsed, pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
4. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

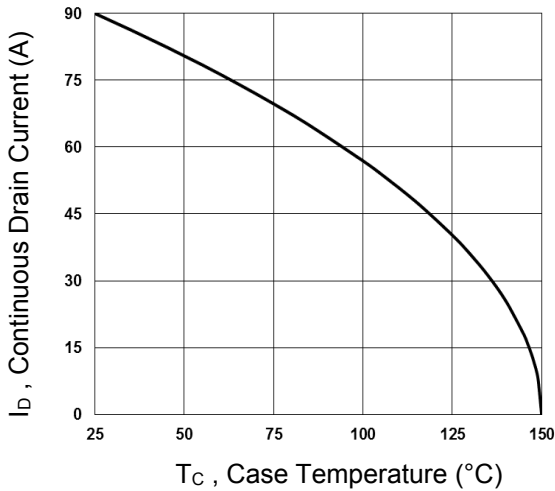


Fig.1 Continuous Drain Current vs. T_c

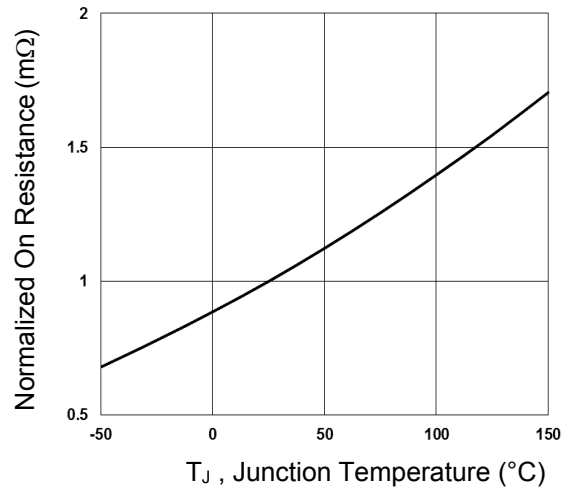


Fig.2 Normalized $R_{DS(ON)}$ vs. T_j

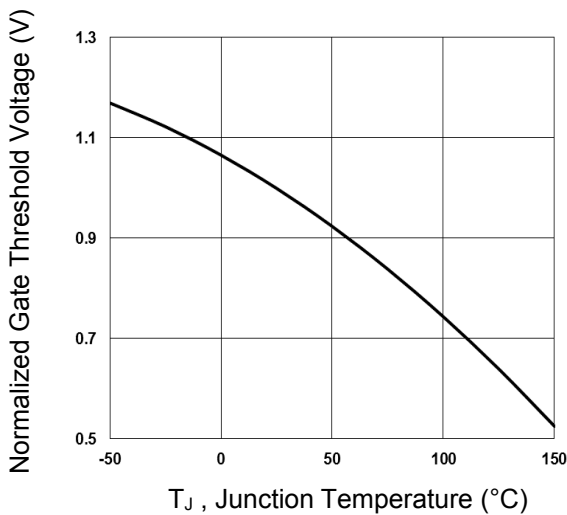


Fig.3 Normalized V_{th} vs. T_j

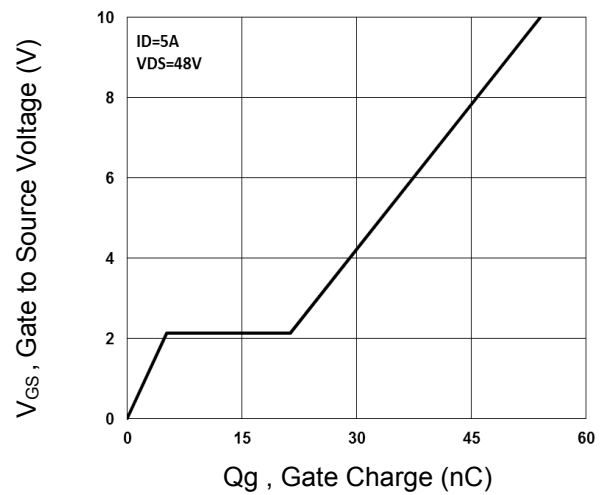


Fig.4 Gate Charge Characteristics

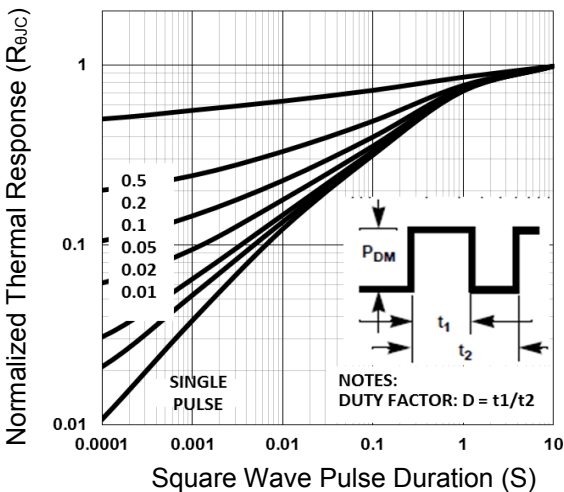


Fig.5 Normalized Transient Impedance

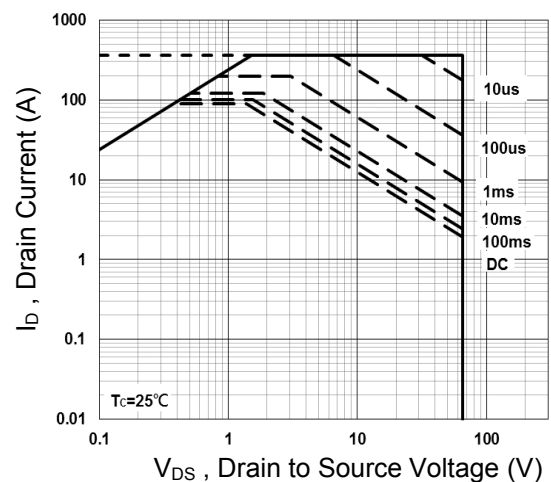


Fig.6 Maximum Safe Operation Area

Typical Electrical and Thermal Characteristic Curves

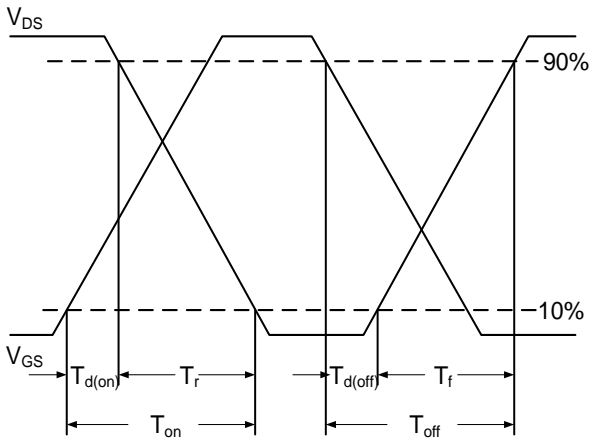


Fig.7 Switching Time Waveform

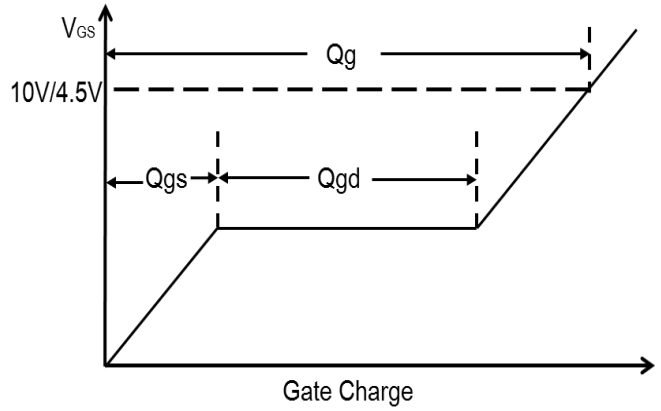
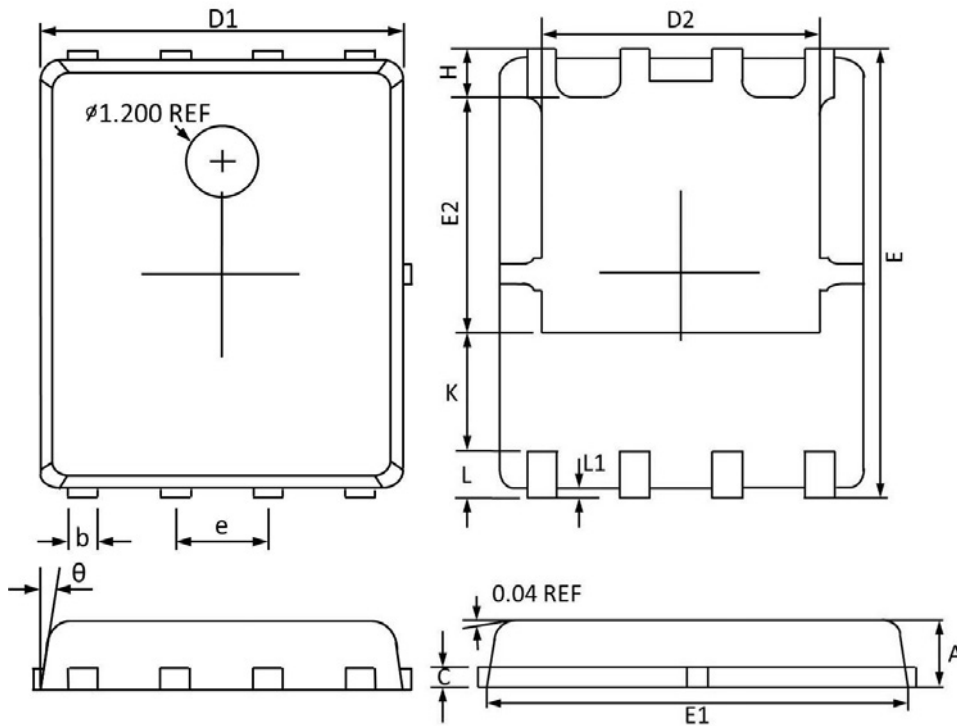


Fig.8 Gate Charge Waveform

Package Outline Dimensions

PPAK5X6



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	1.100	0.800	0.043	0.031
b	0.510	0.330	0.020	0.013
C	0.300	0.200	0.012	0.008
D1	5.100	4.800	0.201	0.189
D2	4.100	3.610	0.161	0.142
E	6.200	5.900	0.244	0.232
E1	5.900	5.700	0.232	0.224
E2	3.780	3.350	0.149	0.132
e	1.27BSC		0.05BSC	
H	0.700	0.410	0.028	0.016
K	1.500	1.100	0.059	0.043
L	0.710	0.510	0.028	0.020
L1	0.200	0.060	0.008	0.002
θ	12°	0°	12°	0°