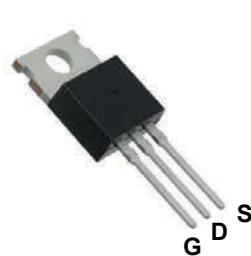
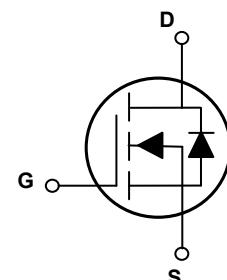


Main Product Characteristics

$V_{(BR)DSS}$	1000V
$R_{DS(ON)}$	5.4Ω (Max.)
I_D	5A



TO-220



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSJH5N100 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Parameter	Unit
Drain-Source Voltage	V_{DS}	1000	V
Gate-to-Source Voltage	V_{GS}	±30	V
Continuous Drain Current, @ Steady-State ($T_C=25^\circ\text{C}$)	I_D	5	A
Pulsed Drain Current	I_{DM}	10	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	133	W
		1.06	W/°C
Single Pulse Avalanche Energy ¹	E_{AS}	173	mJ
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62.5	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.94	°C/W
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	°C

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	1000	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=1000\text{V}, V_{\text{GS}}=0\text{V}$	-	-	10	μA
Gate-to-Source Forward Leakage	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=30\text{V}$	-	-	100	nA
		$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-30\text{V}$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_D=2\text{A}$	-	4.3	5.4	Ω
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	3.0	-	5.0	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}, f=1\text{MHz}$	-	726	-	pF
Output Capacitance	C_{oss}		-	62	-	
Reverse Transfer Capacitance	C_{rss}		-	1.8	-	
Total Gate Charge ^{2,3}	Q_g	$I_D=5\text{A}, I_G=1\text{mA}, V_{\text{DS}}=800\text{V}, V_{\text{GS}}=10\text{V}$	-	12	-	nC
Gate-to-Source Charge ^{2,3}	Q_{gs}		-	6	-	
Gate-to-Drain ("Miller") Charge ^{2,3}	Q_{gd}		-	0.6	-	
Turn-on Delay Time ^{2,3}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=100\text{V}, V_{\text{GS}}=10\text{V}, R_G=25\Omega, I_D=5\text{A}$	-	12	-	nS
Rise Time ^{2,3}	t_r		-	16	-	
Turn-Off Delay Time ^{2,3}	$t_{\text{d}(\text{off})}$		-	24	-	
Fall Time ^{2,3}	t_f		-	30	-	
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	$T_C=25^\circ\text{C}$, MOSFET symbol showing the integral reverse p-n junction diode.	-	-	5	A
Source Pulse Current	I_{SM}		-	-	10	A
Diode Forward Voltage	V_{SD}	$I_S=3\text{A}, V_{\text{GS}}=0\text{V}$	-	-	1.4	V
Reverse Recovery Time	T_{rr}	$I_F=5\text{A}, \text{di}/\text{dt}=100\text{A}/\text{us}$	-	510	-	nS
Reverse Recovery Charge	Q_{rr}		-	9.1	-	μC

Note:

1. $L=30\text{mH}, I_{AS}=3.4\text{A}, V_{DD}=100\text{V}, R_G=25\Omega$, starting temperature $T_J=25^\circ\text{C}$.
2. Pulse test: Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

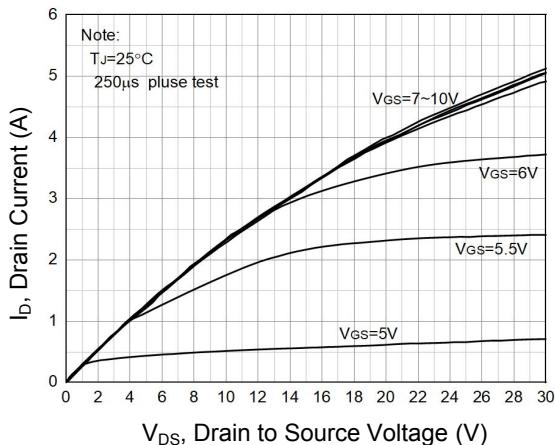


Figure 1. Typical Output Characteristics

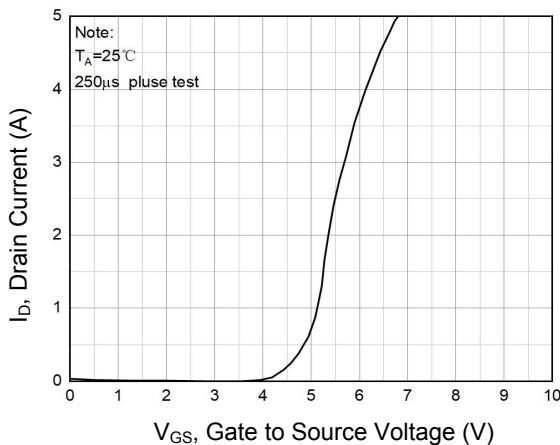


Figure 2. Transfer Characteristics

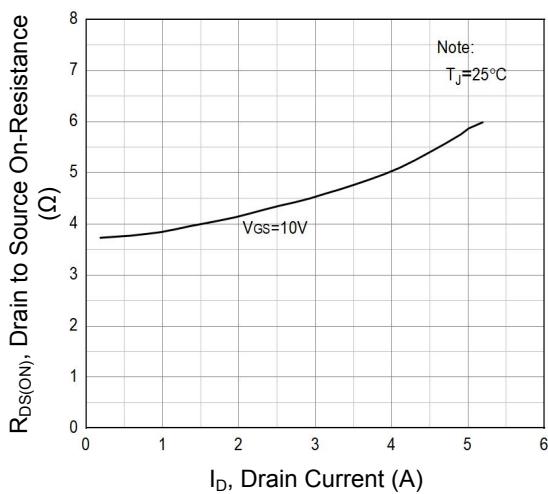


Figure 3. $R_{DS(on)}$ vs. Drain Current

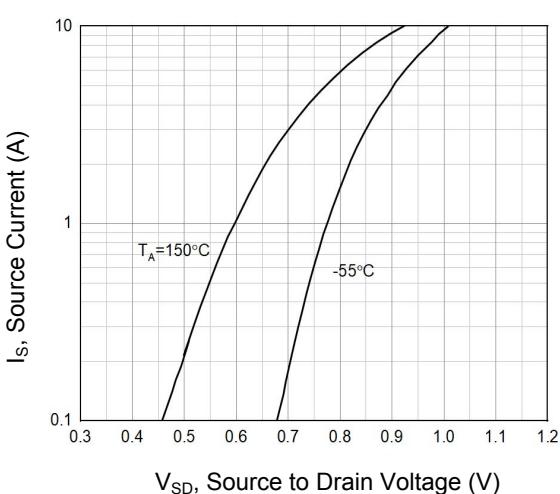


Figure 4. Body Diode Characteristics

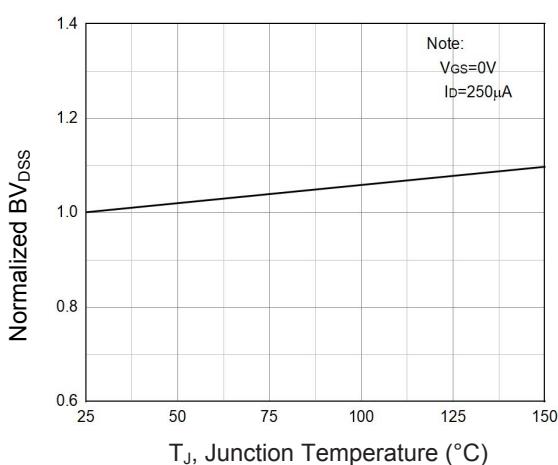


Figure 5. Normalized BV_{dss} vs. T_J

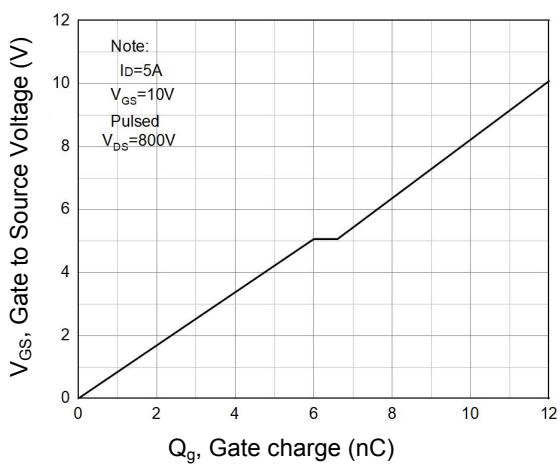


Figure 6. Gate Charge

Typical Electrical and Thermal Characteristic Curves

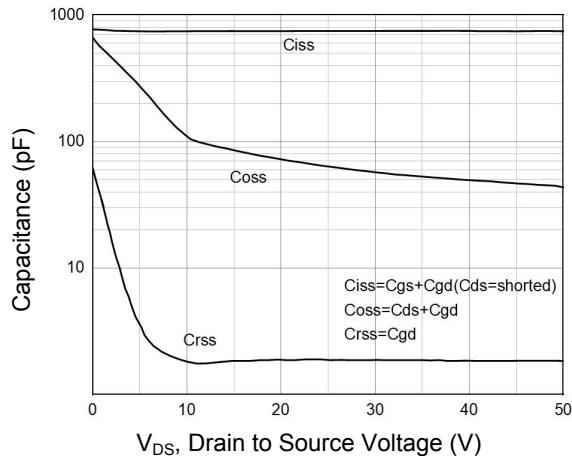


Figure 7. Capacitance Characteristics

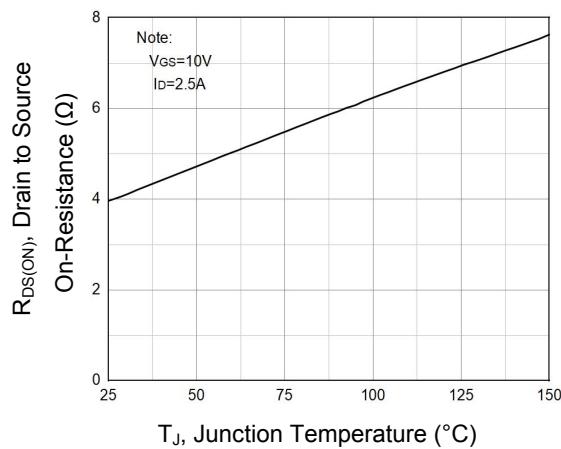


Figure 8. $R_{DS(on)}$ vs. T_J

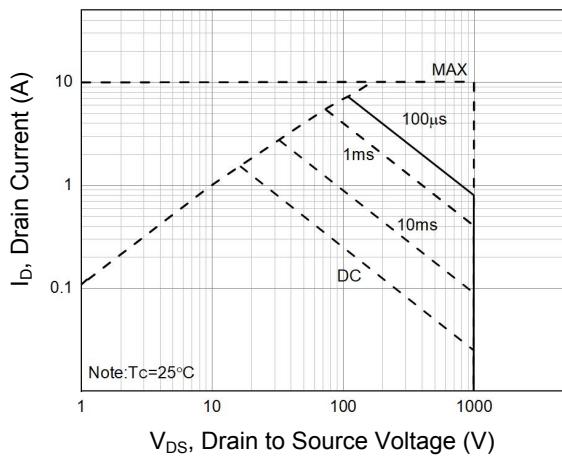


Figure 9. Safe Operation Area

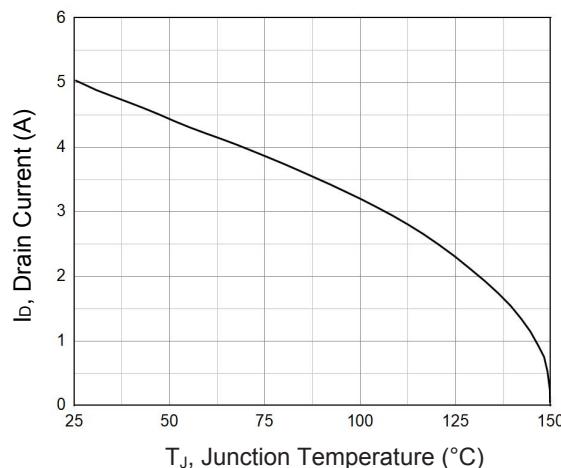
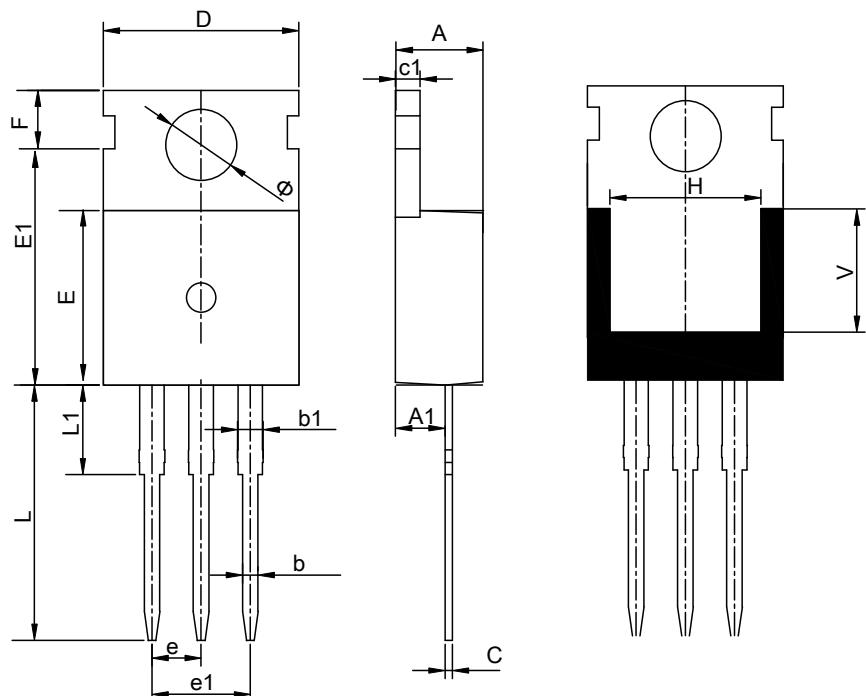


Figure 10. Drain Current vs. T_J

Package Outline Dimensions (TO-220)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.40	4.60	0.173	0.181
A1	2.25	2.70	0.089	0.106
b	0.71	0.91	0.028	0.036
b1	1.17	1.37	0.046	0.054
C	0.33	0.65	0.013	0.026
c1	1.20	1.40	0.047	0.055
D	9.91	10.25	0.390	0.404
E	8.95	9.75	0.352	0.384
E1	12.65	13.00	0.498	0.512
e	2.54 TYP		0.100 TYP	
e1	4.98	5.18	0.196	0.204
F	2.65	2.95	0.104	0.116
H	7.90	8.10	0.311	0.319
L	12.90	13.40	0.508	0.528
L1	2.68	3.25	0.106	0.128
V	6.90 REF		0.272 REF	
Φ	3.40	3.80	0.134	0.150

Order Information

Device	Package	Marking	Carrier	Quantity
GSJH5N100	TO-220	H5N100	Tube	50 pcs / Tube

For more information, please contact us at: inquiry@goodarksemi.com