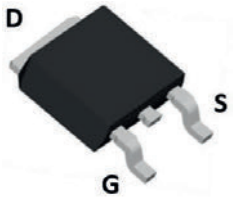
GOOD-ARK SEMICONDUCTOR

GSJD4N60

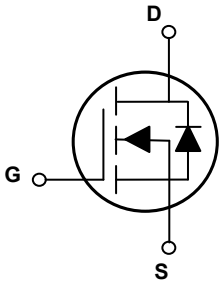
600V N-Channel MOSFET

Main Product Characteristics

$V_{(BR)DSS}$	600V
$R_{DS(ON)}$	2.4Ω (Max.)
I_D	4A



TO-252 (DPAK)



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

RoHS Compliant

Description

The GSJD4N60 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings (T_C=25°C unless otherwise specified)

Parameter	Symbol	Parameter	Unit
Drain-Source Voltage	V_{DS}	600	V
Gate-to-Source Voltage	V_{GS}	±30	V
Continuous Drain Current, @ Steady-State (T_C=25°C)	I_D	4.0	A
Continuous Drain Current, @ Steady-State (T_C=100°C)		2.5	A
Pulsed Drain Current	I_{DM}	16	A
Power Dissipation (T_C=25°C)	P_D	77	W
		0.62	W/°C
Single Pulse Avalanche Energy[1]	E_{AS}	216	mJ
Thermal Resistance, Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62.0	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	1.61	°C/W
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	°C

Electrical Characteristics (T_C=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	V_{GS}=0V, I_D=250μA	600	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	V_{DS}=600V, V_{GS}=0V	-	-	1.0	uA
Gate-to-Source Forward Leakage	I_{GSS}	V_{DS}=0V, V_{GS}=30V	-	-	100	nA
		V_{DS}=0V, V_{GS}=-30V	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=2A	-	2.0	2.4	Ω
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA	2.1	-	3.9	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	V_{GS}=0V, V_{DS}=25V, f=1MHz	-	434	-	pF
Output Capacitance	C_{oss}		-	54	-	
Reverse Transfer Capacitance	C_{rss}		-	4.6	-	
Total Gate Charge[2,3]	Q_g	I_D=4A, V_{DD}=480V, V_{GS}=10V	-	13	-	nC
Gate-to-Source Charge[2,3]	Q_{gs}		-	2.8	-	
Gate-to-Drain ("Miller") Charge[2,3]	Q_{gd}		-	6.2	-	
Turn-On Delay Time[2,3]	$t_{d(on)}$	V_{DD}=300V, R_G=25Ω, I_D=4A	-	10.5	-	nS
Rise Time[2,3]	t_r		-	26	-	
Turn-Off Delay Time[2,3]	$t_{d(off)}$		-	28	-	
Fall Time[2,3]	t_f		-	26	-	
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	T_C=25°C, MOSFET symbol showing the integral reverse p-n junction diode.	-	-	4.0	A
Source Pulse Current	I_{SM}		-	-	16	A
Diode Forward Voltage	V_{SD}	I_S=4A, V_{GS}=0V	-	-	1.4	V
Reverse Recovery Time[2]	T_{rr}	I_S=4A, V_{GS}=0V, dI_F/dt=100A/us	-	423	-	nS
Reverse Recovery Charge[2]	Q_{rr}		-	1.8	-	μC

Note:

1. L=30mH, I_{AS}=3.75A, V_{DD}=100V, R_G=25Ω, starting temperature T_J=25°C.
2. Pulse test: Pulse width ≤ 300μs, duty cycle ≤ 2%.
3. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

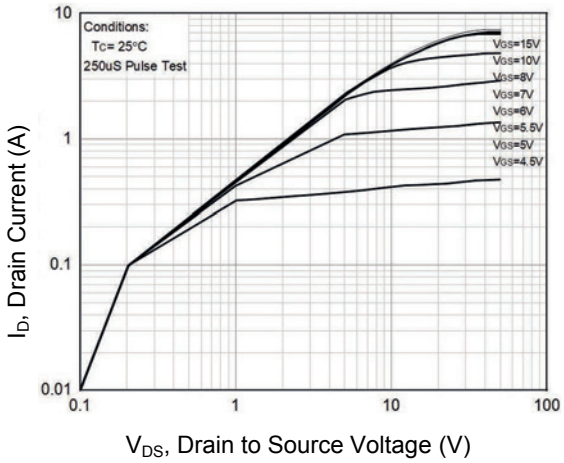


Figure 1. Typical Output Characteristics

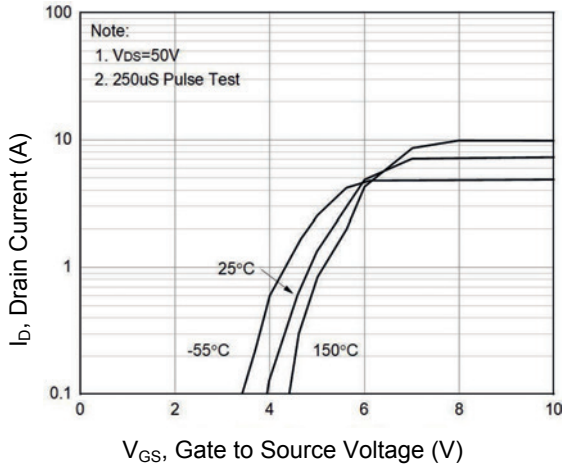


Figure 2. Transfer Characteristics

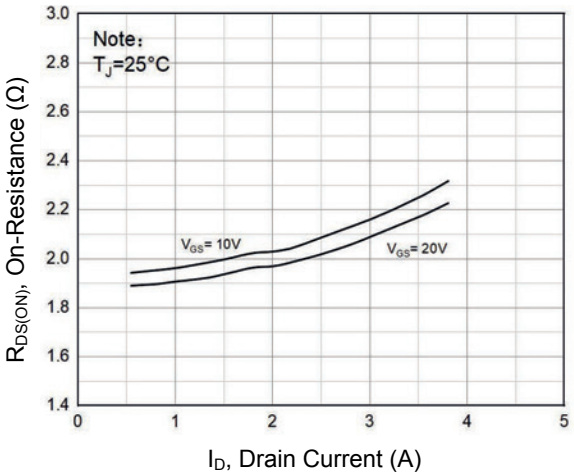


Figure 3. $R_{DS(ON)}$ vs. Drain Current

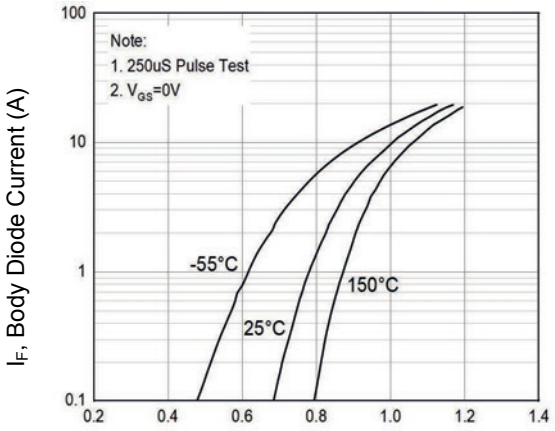


Figure 4. Body Diode Characteristics

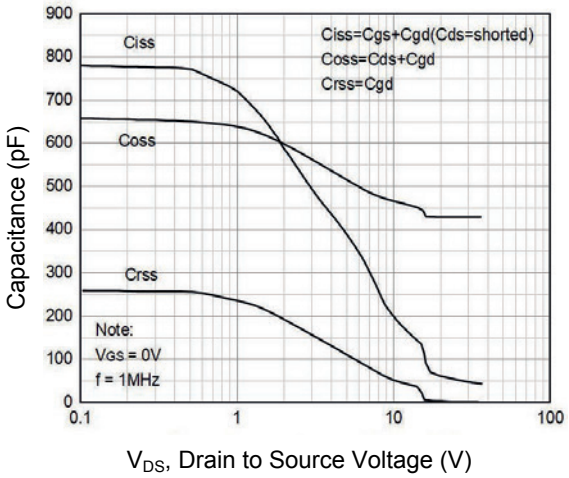


Figure 5. Capacitance Characteristics

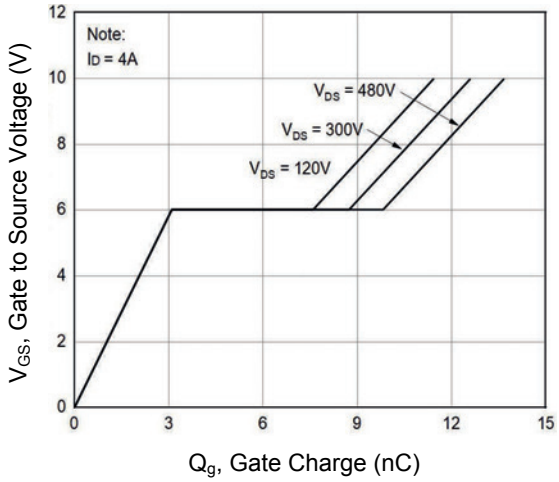


Figure 6. Gate Charge Characteristics

Typical Electrical and Thermal Characteristic Curves

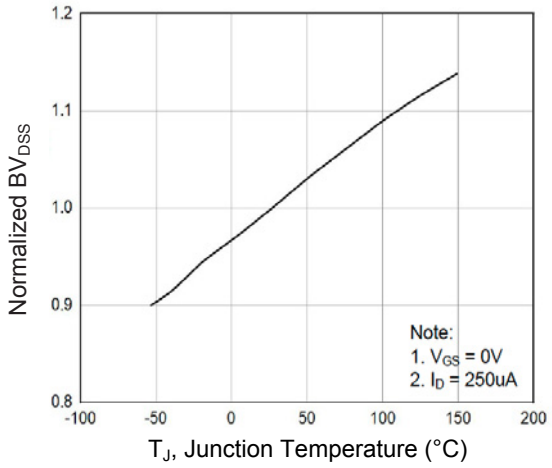


Figure 7. Normalized BV_{DSS} vs. T_J

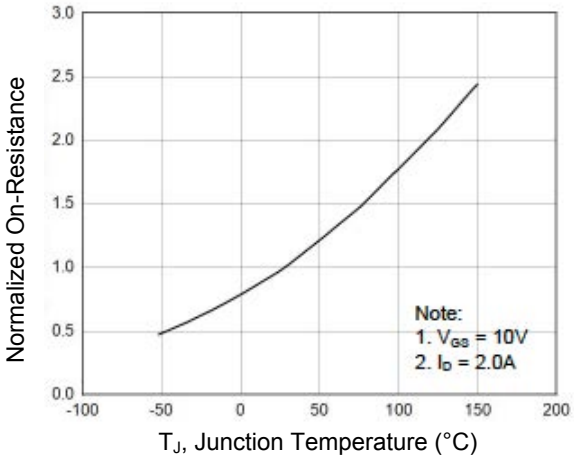


Figure 8. Normalized $R_{DS(ON)}$ vs. T_J

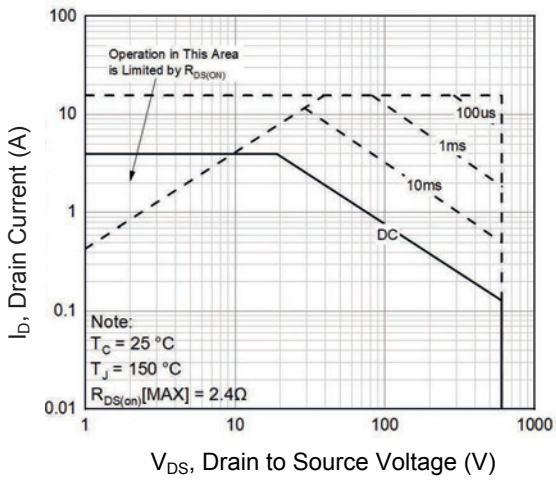


Figure 9. Safe Operation Area

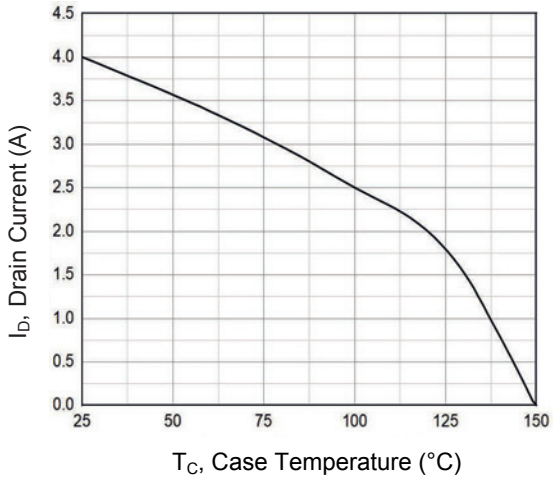
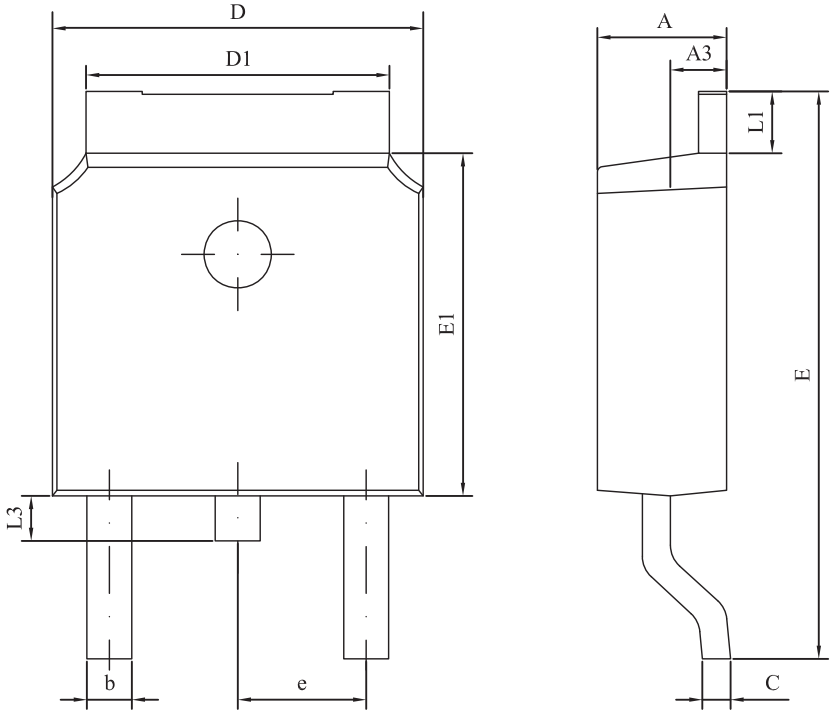


Figure 10. Drain Current vs. Case Temperature

Package Outline Dimensions TO-252(DPAK)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	2.15	2.40	0.085	0.094
A3	0.90	1.10	0.035	0.043
b	0.50	0.90	0.020	0.035
C	0.40	0.65	0.016	0.026
D	6.30	6.90	0.248	0.272
D1	4.95	5.50	0.195	0.217
E	9.40	10.41	0.370	0.410
E1	5.90	6.30	0.232	0.248
e	2.286 BSC		0.090 BSC	
L1	0.89	1.27	0.035	0.050
L3	0.60	1.10	0.024	0.043

Order Information

Device	Package	Marking	Carrier	Quantity
GSJD4N60	TO-252 (DPAK)	D4N60	Tape & Reel	2,500 pcs / Reel