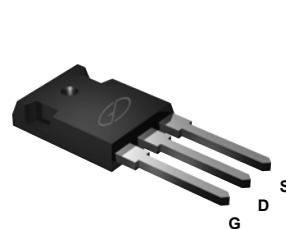
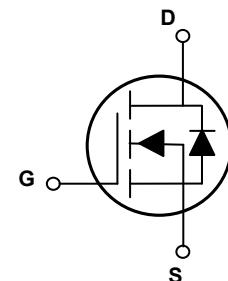


## Main Product Characteristics

$V_{(BR)DSS}$	650V
$R_{DS(ON)}$	110mΩ (Max.)
$I_D$	35A



TO-247



Schematic Diagram

## Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



## Description

The GSJA65RF110 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

## Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Parameter	Unit
Drain-Source Voltage	$V_{DS}$	650	V
Gate-to-Source Voltage	$V_{GS}$	$\pm 30$	V
Continuous Drain Current, @ Steady-State ( $T_C=25^\circ\text{C}$ )	$I_D$	35	A
Continuous Drain Current, @ Steady-State ( $T_C=100^\circ\text{C}$ )		12	A
Pulsed Drain Current	$I_{DM}$	140	A
Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	278	W
		2.23	W/ $^\circ\text{C}$
Single Pulse Avalanche Energy <sup>1</sup>	$E_{AS}$	1136	mJ
Body Diode Reverse Voltage Slope <sup>2</sup>	$dv/dt$	50	V/ns
MOS $dv/dt$ Ruggedness <sup>3</sup>	$dv/dt$	50	V/ns
Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction-to-Case	$R_{\theta JC}$	0.45	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	$T_J/T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On / Off Characteristics</b>						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	650	-	-	V
Drain-to-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=650\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
Gate-to-Source Forward Leakage	$I_{\text{GSS}}$	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=20\text{V}$	-	-	100	nA
		$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-20\text{V}$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_D=17\text{A}$	-	90	110	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	3.1	-	4.9	V
<b>Dynamic and Switching Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}, f=1\text{MHz}$	-	3011	-	pF
Output Capacitance	$C_{\text{oss}}$		-	84	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	5.5	-	
Total Gate Charge <sup>4,5</sup>	$Q_g$	$I_D=19\text{A}, V_{\text{DD}}=480\text{V}, V_{\text{GS}}=10\text{V}$	-	81	-	nC
Gate-to-Source Charge <sup>4,5</sup>	$Q_{\text{gs}}$		-	28	-	
Gate-to-Drain ("Miller") Charge <sup>4,5</sup>	$Q_{\text{gd}}$		-	42	-	
Gate-to-Plateau Voltage <sup>4,5</sup>	$V_{\text{plateau}}$		-	8.4	-	V
Turn-On Delay Time <sup>4,5</sup>	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=400\text{V}, V_{\text{GS}}=10\text{V}, R_G=1.8\Omega, I_D=19\text{A}$	-	24	-	nS
Rise Time <sup>4,5</sup>	$t_r$		-	27	-	
Turn-Off Delay Time <sup>4,5</sup>	$t_{\text{d}(\text{off})}$		-	59	-	
Fall Time <sup>4,5</sup>	$t_f$		-	23	-	
Gate Resistance	$R_g$	$f=1\text{MHz}$	-	1.82	-	$\Omega$
<b>Source-Drain Ratings and Characteristics</b>						
Continuous Source Current (Body Diode)	$I_S$	$T_C=25^\circ\text{C}$ , MOSFET symbol showing the integral reverse p-n junction diode.	-	-	35	A
Source Pulse Current	$I_{\text{SM}}$		-	-	140	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_S=19\text{A}, V_{\text{GS}}=0\text{V}$	-	1.1	1.4	V
Reverse Recovery Time <sup>2</sup>	$T_{\text{rr}}$	$I_F=19\text{A}, V_{\text{DD}}=400\text{V}, dI_F/dt=100\text{A}/\mu\text{s}$	-	104	-	nS
Reverse Recovery Charge <sup>2</sup>	$Q_{\text{rr}}$		-	0.46	-	$\mu\text{C}$
Reverse Recovery Current <sup>2</sup>	$I_{\text{rrm}}$		-	8	-	A

Notes:

1.  $L=79\text{mH}, V_{\text{DD}}=100\text{V}, R_G=25\Omega$ , starting temperature  $T_J=25^\circ\text{C}$ .
2.  $V_{\text{DS}}=0-400\text{V}, I_{\text{SD}} \leq I_S, T_J=25^\circ\text{C}$ .
3.  $V_{\text{DS}}=0-480\text{V}$ .
4. Pulse test: pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
5. Essentially independent of operating temperature.

## Typical Electrical and Thermal Characteristic Curves

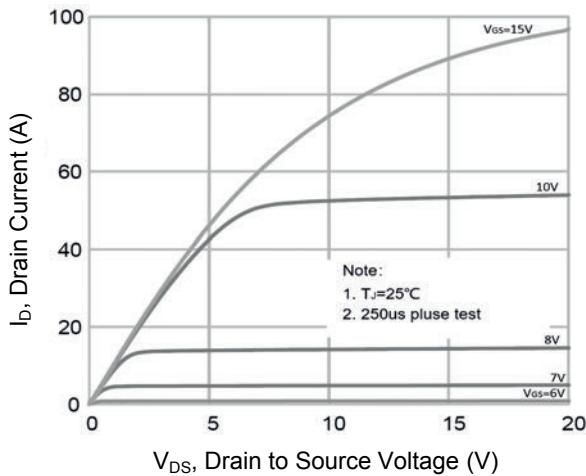


Figure 1. Typical Output Characteristics

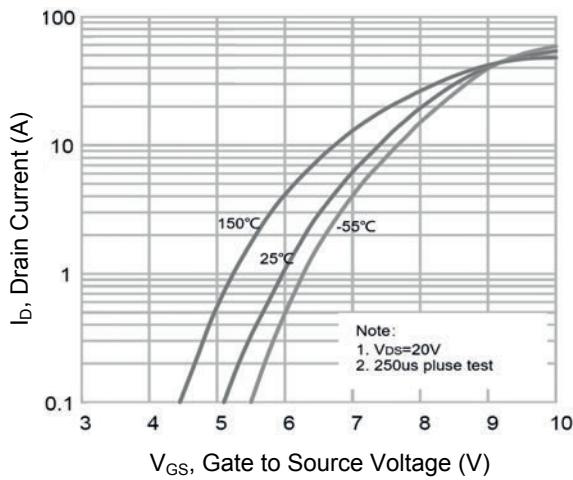


Figure 2. Transfer Characteristics

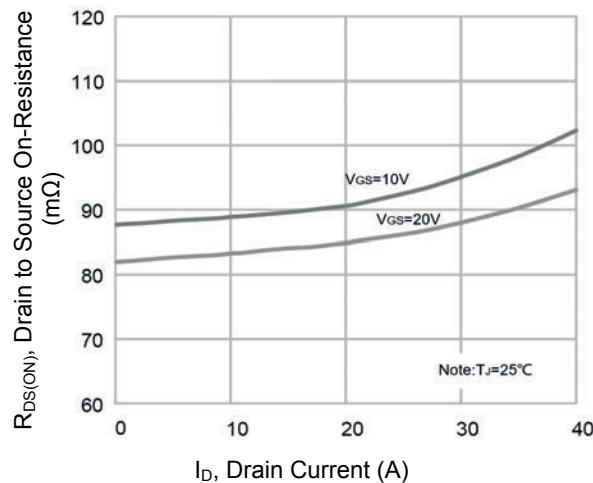


Figure 3.  $R_{DS(ON)}$  Vs. Drain Current

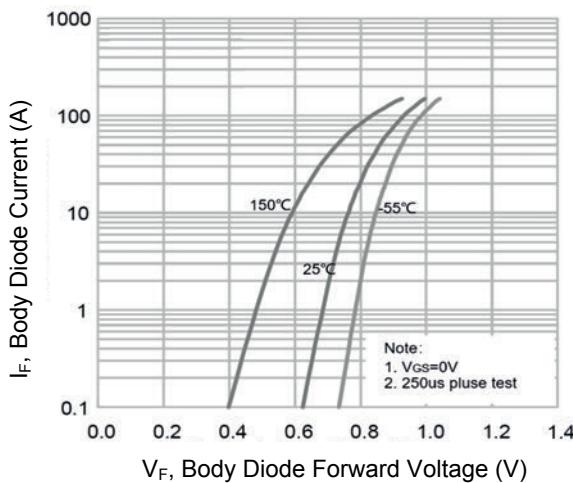


Figure 4. Body Diode Characteristics

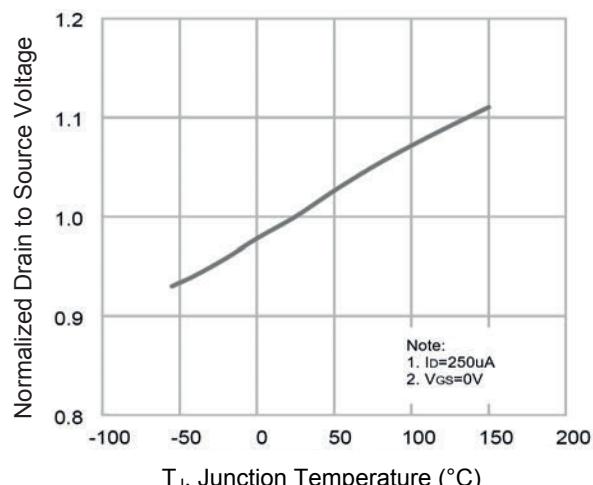


Figure 5. Normalized  $BV_{DSS}$  Vs.  $T_J$

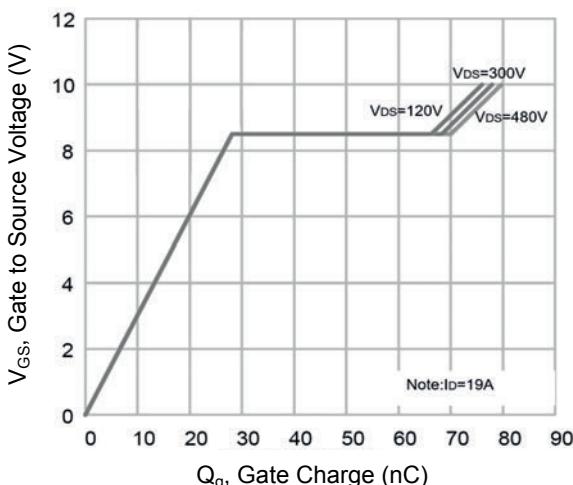
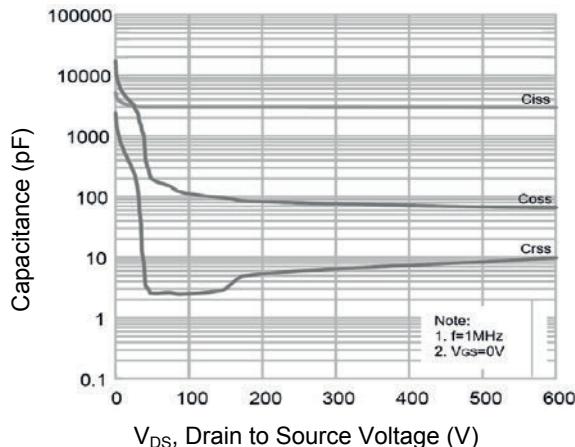
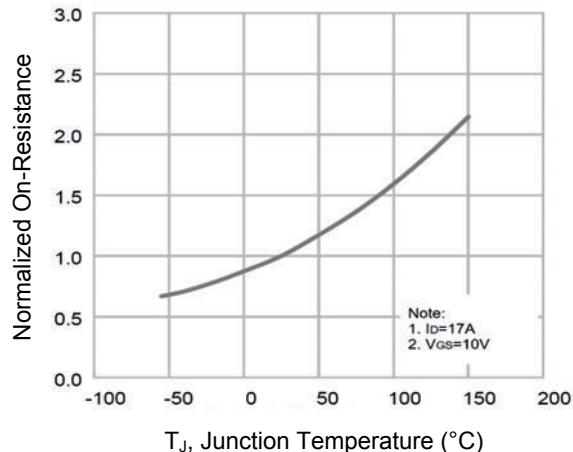


Figure 6. Gate Charge

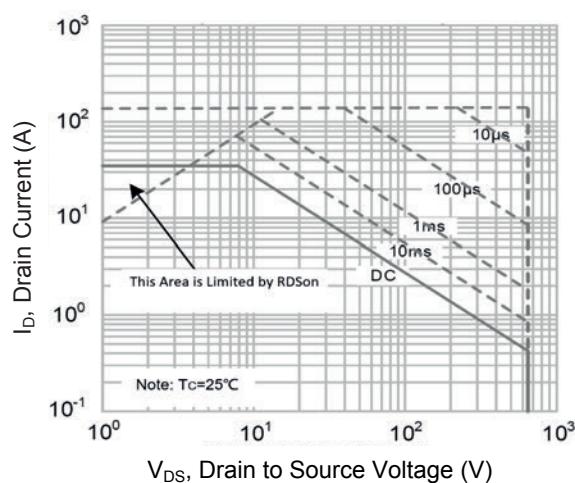
## Typical Electrical and Thermal Characteristic Curves



**Figure 7. Capacitance Characteristics**

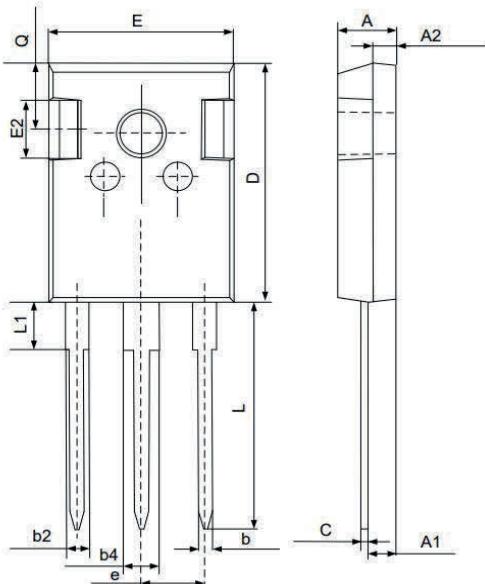


**Figure 8. Normalized  $R_{DS(ON)}$  Vs.  $T_J$**



**Figure 9. Safe Operation Area**

## Package Outline Dimensions (TO-247)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	4.80	5.20	0.189	0.205
A1	2.21	2.59	0.087	0.102
A2	1.85	2.15	0.073	0.085
b	1.11	1.36	0.044	0.054
b2	1.91	2.25	0.075	0.089
b4	2.91	3.25	0.115	0.128
C	0.51	0.75	0.020	0.030
D	20.80	21.30	0.819	0.839
E	15.50	16.10	0.610	0.634
E2	4.40	5.20	0.173	0.205
e	5.44 BSC		0.214 BSC	
L	19.72	20.22	0.776	0.796
L1	-	4.30	-	0.169
Q	5.60	6.00	0.220	0.236

## Order Information

Device	Package	Marking	Carrier	Quantity
GSJA65RF110	TO-247	A65RF110	Tube	30 pcs / Tube

For more information, please contact us at: [inquiry@goodarksemi.com](mailto:inquiry@goodarksemi.com)