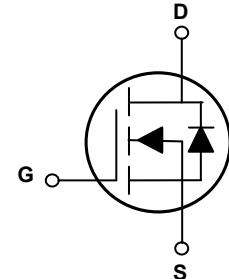


Main Product Characteristics

$V_{(BR)DSS}$	40V
$R_{DS(ON)}$	6.2mΩ (Max.)
I_D	41A



PPAK3x3



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSGN6R204 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, @ Steady-State ($T_C=25^\circ\text{C}$)	I_D	41	A
Continuous Drain Current, @ Steady-State ($T_C=100^\circ\text{C}$)		28	A
Pulsed Drain Current ($T_C=25^\circ\text{C}$) ¹	I_{DM}	164	A
Power Dissipation ($T_C=25^\circ\text{C}$) ²	P_D	39	W
Single Pulse Avalanche Energy ⁵	E_{AS}	33.6	mJ
Single Pulse Current	I_{AS}	11.6	A
Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	40	$^\circ\text{C}/\text{W}$
Junction-to-Case	$R_{\theta JC}$	3.2	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	$^\circ\text{C}$
Soldering Temperature (SMD)	T_{sold}	260	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	40	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1.0	μA
		$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$	-	2.0	-	
Gate-to-Source Forward Leakage	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=\pm20\text{V}$	-	-	±100	nA
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_D=20\text{A}$	-	5.1	6.2	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=20\text{A}$	-	7.3	9.5	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	1.1	-	2.8	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=15\text{V}, f=1\text{MHz}$	-	860	-	pF
Output Capacitance	C_{oss}		-	495	-	
Reverse Transfer Capacitance	C_{rss}		-	36	-	
Total Gate Charge ^{3,4}	Q_g	$I_D=9\text{A}, V_{\text{DD}}=15\text{V}, V_{\text{GS}}=10\text{V}$	-	15.5	-	nC
Gate-to-Source Charge ^{3,4}	Q_{gs}		-	3.2	-	
Gate-to-Drain ("Miller") Charge ^{3,4}	Q_{gd}		-	2.2	-	
Gate to Plateau ^{3,4}	V_{plateau}		-	3.3	-	V
Turn-on Delay Time ^{3,4}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=20\text{V}, V_{\text{GS}}=10\text{V}, R_G=3\Omega, I_D=9\text{A}$	-	4.4	-	nS
Rise Time ^{3,4}	t_r		-	31	-	
Turn-Off Delay Time ^{3,4}	$t_{\text{d}(\text{off})}$		-	21	-	
Fall Time ^{3,4}	t_f		-	12	-	
Gate Resistance	R_g	$f=1\text{MHz}$	-	2.2	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	MOSFET symbol showing the integral reverse p-n junction diode.	-	-	41	A
Diode Pulse Current	$I_{\text{S,pulse}}$		-	-	164	A
Diode Forward Voltage	V_{SD}	$I_S=5\text{A}, V_{\text{GS}}=0\text{V}$	-	-	1.4	V
Reverse Recovery Time ³	T_{rr}	$I_S=2\text{A}, V_{\text{GS}}=0\text{V}, V_R=30\text{V}, dI_F/dt=100\text{A/us}$	-	29	-	nS
Reverse Recovery Charge ³	Q_{rr}		-	14	-	nC

Notes:

1. Pulse time of 5 μs .
2. The dissipated power value will change with the temperature. When it is greater than 25°C, the dissipated power value will decrease by 0.55°C/W for every 1 degree of temperature increase.
3. Pulse test: Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
4. Basically unaffected by operating temperature.
5. $L=0.5\text{mH}, R_G=25\Omega, V_{\text{DD}}=24\text{V}, T_J=25^\circ\text{C}$.

Typical Electrical and Thermal Characteristic Curves

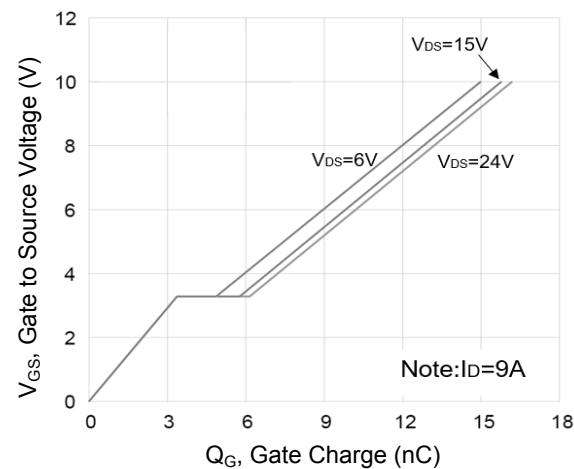
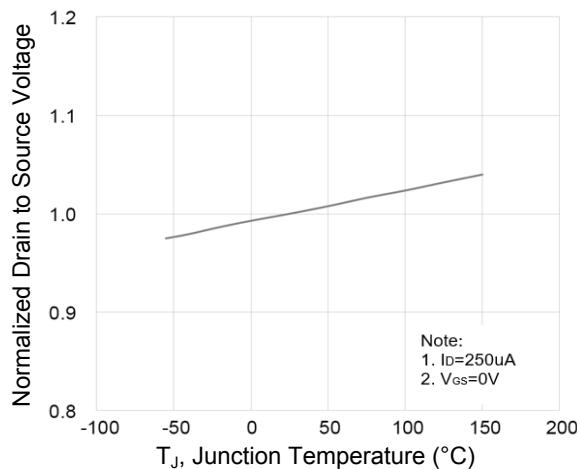
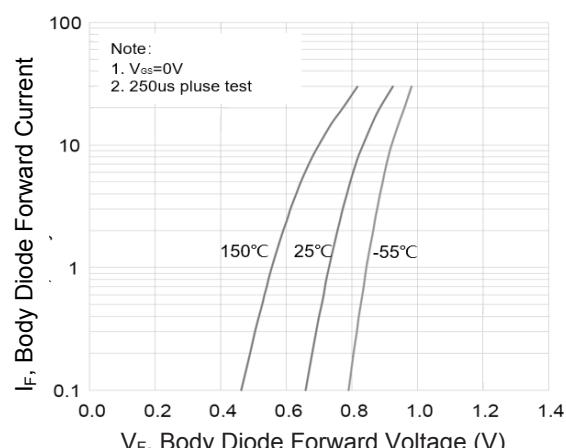
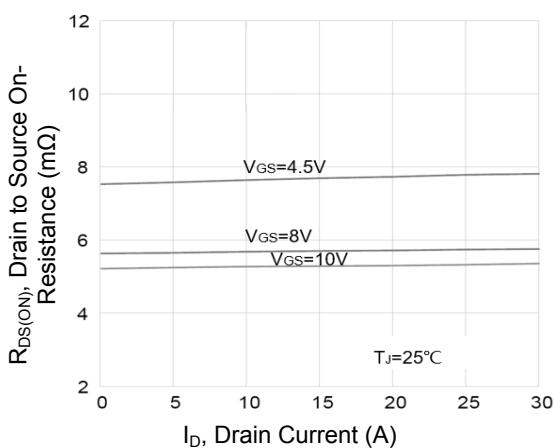
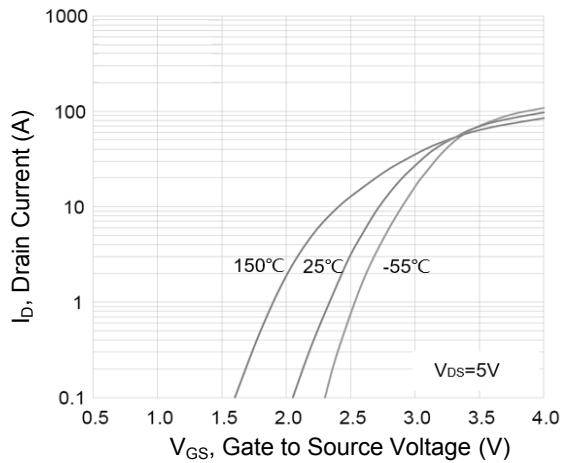
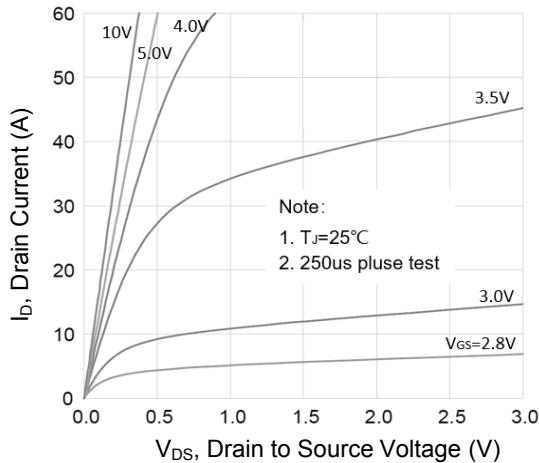


Figure 5. Normalized BV_{DSS} vs. T_J

Figure 6. Gate Charge

Typical Electrical and Thermal Characteristic Curves

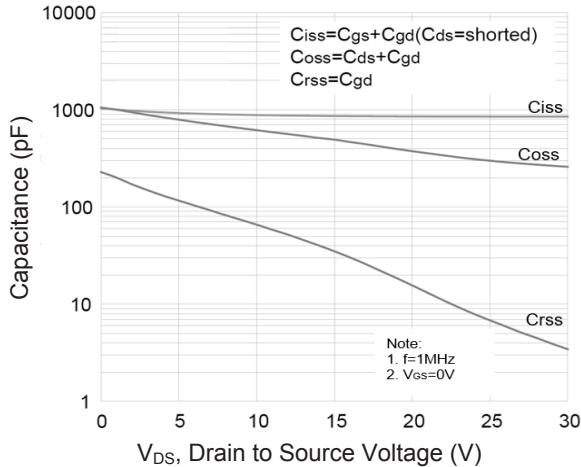


Figure 7. Capacitance Characteristics

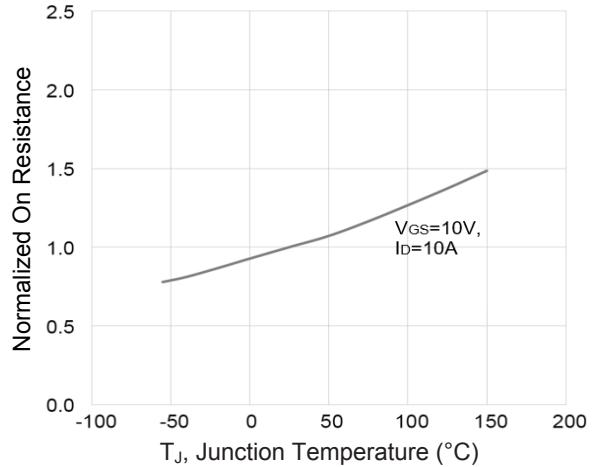


Figure 8. Normalized $R_{DS(ON)}$ vs. T_J

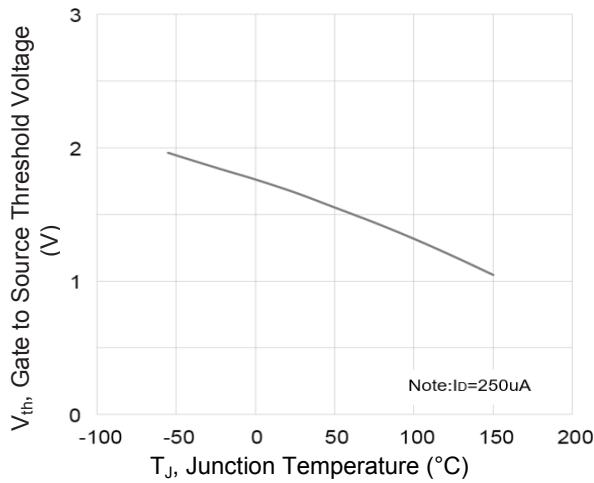


Figure 9. V_{th} vs. T_J

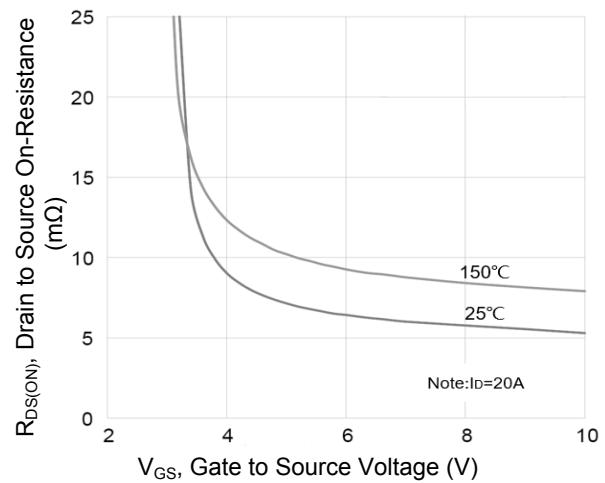
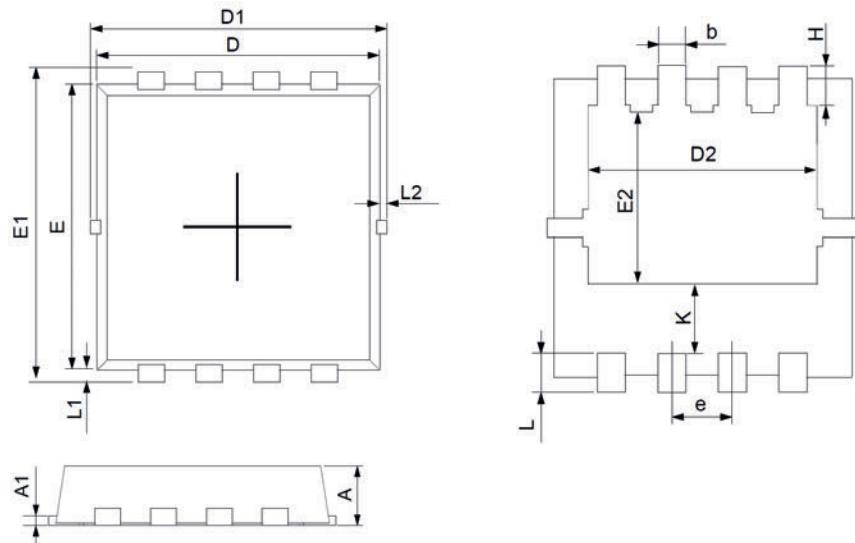


Figure 10. $R_{DS(ON)}$ vs. Gate to Source Voltage

Package Outline Dimensions (PPAK3x3)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.70	0.90	0.028	0.035
A1	0.14	0.20	0.006	0.008
D	3.05	3.25	0.120	0.128
E	2.90	3.10	0.114	0.122
D1	3.10	3.50	0.122	0.138
D2	2.35	2.50	0.093	0.098
E1	3.10	3.50	0.122	0.138
E2	1.64	1.84	0.065	0.072
b	0.25	0.35	0.010	0.014
k	0.59	0.79	0.023	0.031
e	0.55	0.75	0.022	0.030
E4	3.34	3.92	0.131	0.154
L	0.25	0.55	0.010	0.022
L1	0.10	0.20	0.004	0.008
H	0.32	0.52	0.013	0.020

Order Information

Device	Package	Marking	Carrier	Quantity
GSGN6R204	PPAK3x3	N6R204	Tape & Reel	5,000pcs / Reel