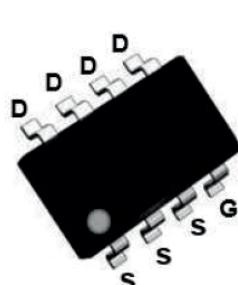
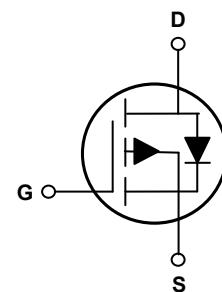


Main Product Characteristics

BV _{DSS}	-30V
R _{DS(ON)}	41mΩ (Max.)
I _D	-5.6A



SOP-8



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFQ3041 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V _{DS}	-30	V
Gate-Source Voltage	V _{GS}	±20	V
Drain Current-Continuous ($T_c=25^\circ\text{C}$)	I _D	-5.6	A
Drain Current-Continuous ($T_c=100^\circ\text{C}$)		-3.6	
Drain Current-Pulsed ¹	I _{DM}	-22.4	A
Single Pulse Avalanche Energy ²	E _{AS}	30	mJ
Single Pulse Avalanche Current ²	I _{AS}	-10.8	A
Power Dissipation ($T_c=25^\circ\text{C}$)	P _D	2.7	W
Power Dissipation-Derate above 25°C		0.022	W/°C
Thermal Resistance, Junction-to-Ambient	R _{θJA}	60	°C/W
Thermal Resistance, Junction-to-Case	R _{θJC}	47	°C/W
Operating Junction Temperature Range	T _J	-55 To +150	°C
Storage Temperature Range	T _{STG}	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-30	-	-	V
BV_{DSS} Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Reference to 25°C , $I_{\text{D}}=-1\text{mA}$	-	-0.03	-	$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	-1	μA
		$V_{\text{DS}}=-24\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$	-	-	-100	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$	-	30	41	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-4\text{A}$	-	41	54	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=-250\mu\text{A}$	-1.1	-1.7	-2.8	V
$V_{\text{GS}(\text{th})}$ Temperature Coefficient	$\Delta V_{\text{GS}(\text{th})}$		-	4	-	$\text{mV}/^\circ\text{C}$
Forward Transconductance	g_{fs}	$V_{\text{DS}}=-5\text{V}, I_{\text{D}}=-5\text{A}$	-	8.1	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{2,3}	Q_g	$V_{\text{DS}}=-15\text{V}, I_{\text{D}}=-5\text{A}, V_{\text{GS}}=-10\text{V}$	-	15	-	nC
Gate-Source Charge ^{2,3}	Q_{gs}		-	4	-	
Gate-Drain Charge ^{2,3}	Q_{gd}		-	2	-	
Turn-On Delay Time ^{2,3}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-15\text{V}, R_{\text{G}}=3\Omega, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-5\text{A}$	-	13	-	nS
Rise Time ^{2,3}	t_r		-	3.4	-	
Turn-Off Delay Time ^{2,3}	$t_{\text{d}(\text{off})}$		-	41	-	
Fall Time ^{2,3}	t_f		-	9	-	
Input Capacitance	C_{iss}	$V_{\text{DS}}=-15\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	739	-	pF
Output Capacitance	C_{oss}		-	54	-	
Reverse Transfer Capacitance	C_{rss}		-	44	-	
Source-Drain Ratings and Characteristics						
Continuous Source Current	I_s	$V_G=V_D=0\text{V}, \text{Force Current}$	-	-	-5.6	A
Pulsed Source Current	I_{SM}		-	-	-22.4	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_s=-3\text{A}, T_J=25^\circ\text{C}$	-	-	-1.2	V

Notes:

- Repetitive rating: Pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=-10\text{V}, L=0.5\text{mH}, R_g=25\Omega$, starting $T_J=25^\circ\text{C}$.
- Pulse test: Pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.

Typical Electrical and Thermal Characteristic Curves

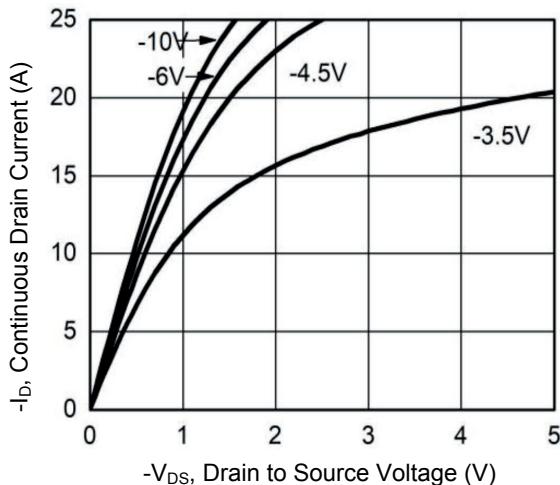


Figure 1. Output Characteristics

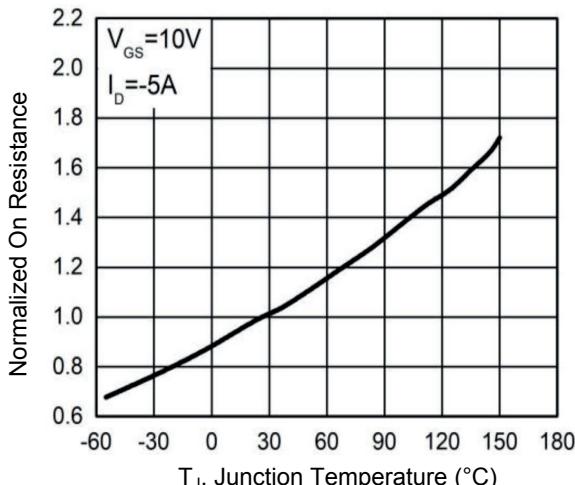


Figure 2. Normalized $R_{DS(ON)}$ vs. T_J

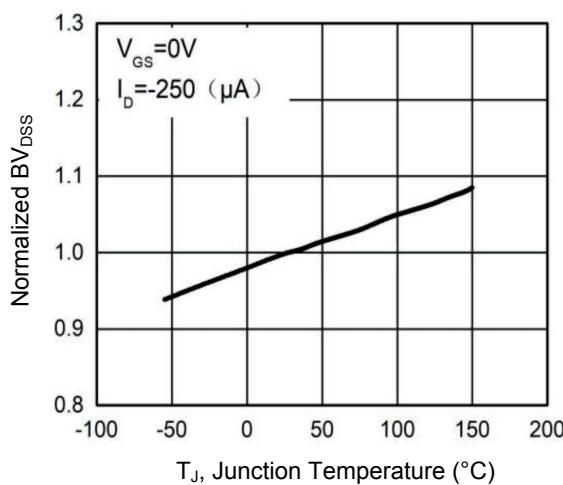


Figure 3. Normalized BV_{DSS} vs. T_J

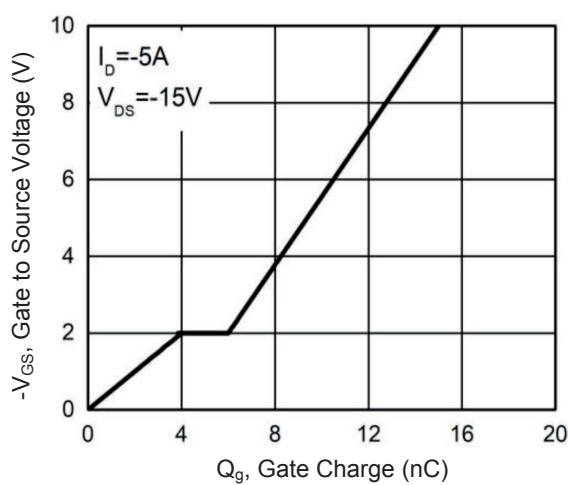


Figure 4. Gate Charge Waveform

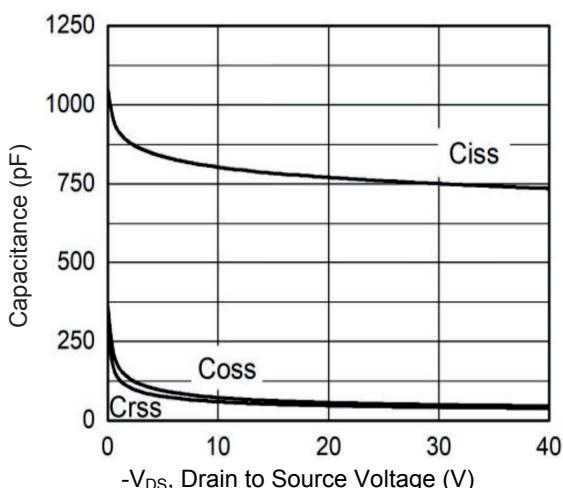


Figure 5. Capacitance Characteristics

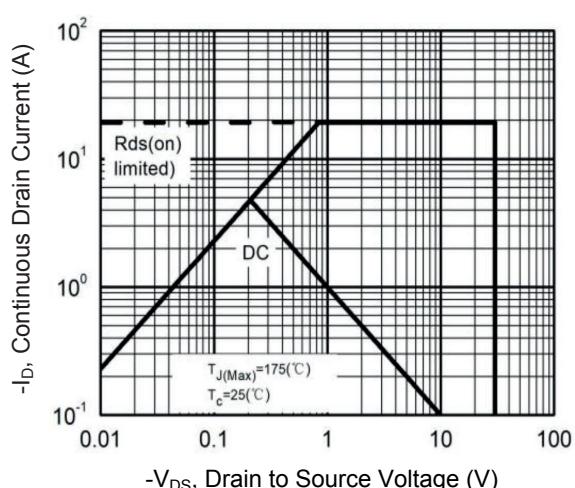
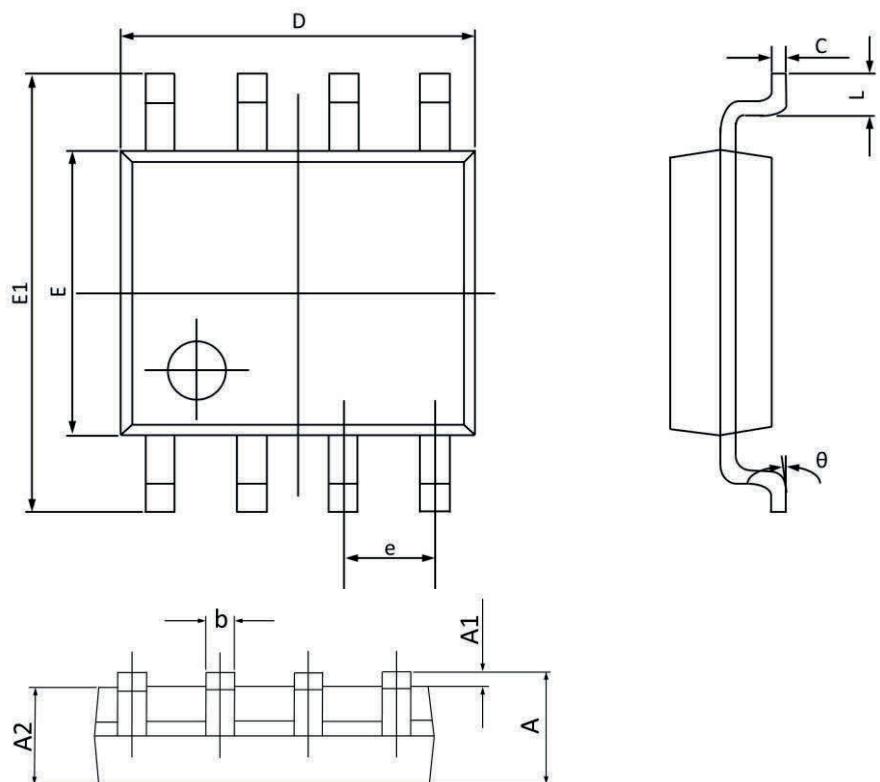


Figure 6. Maximum Safe Operation Area

Package Outline Dimensions (SOP-8)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.300	1.500	0.051	0.059
b	0.350	0.490	0.014	0.019
C	0.190	0.260	0.007	0.010
D	4.700	5.100	0.185	0.201
E	3.700	4.100	0.146	0.161
E1	5.800	6.200	0.228	0.244
e	1.27 BSC		0.05 BSC	
L	0.400	0.900	0.016	0.035
θ	0°	8°	0°	8°

Order Information

Device	Package	Marking	Carrier	Quantity
GSFQ3041	SOP-8	Q3041	Tape & Reel	3,000 Pcs / Reel