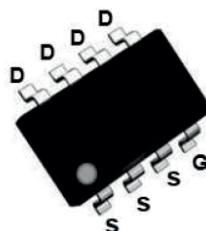
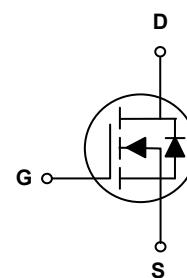


Main Product Characteristics

$V_{(BR)DSS}$	250V
$R_{DS(ON)}$	0.26Ω (Max.)
I_D	4.4A



SOP-8



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFQ2504 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	250	V
Gate-to-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current, @ Steady-State ($T_C=25^\circ\text{C}$)	I_D	4.4	A
Continuous Drain Current, @ Steady-State ($T_C=100^\circ\text{C}$)		2.8	A
Pulsed Drain Current ¹	I_{DM}	17.6	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	6.2	W
		0.049	W/ $^\circ\text{C}$
Single Pulse Avalanche Energy	E_{AS}	240	mJ
Body Diode Reverse Voltage Slope	dv/dt	4.5	V/ns
MOS dv/dt Ruggedness	dv/dt	50	V/ns
Thermal Resistance, Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	20.2	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	$^\circ\text{C}$
Maximum Lead Temperature for Soldering Purposes	T_L	260	$^\circ\text{C}$

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	250	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=250\text{V}, V_{\text{GS}}=0\text{V}, T_C=25^\circ\text{C}$	-	-	1.0	μA
		$V_{\text{DS}}=200\text{V}, T_C=125^\circ\text{C}$	-	-	100	μA
Gate-to-Source Leakage Current	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=30\text{V}$	-	-	100	nA
		$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-30\text{V}$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_D=3\text{A}$	-	0.22	0.26	Ω
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	3.0	-	4.4	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=25\text{V}, f=1\text{MHz}$	-	673	-	pF
Output Capacitance	C_{oss}		-	107	-	
Reverse Transfer Capacitance	C_{rss}		-	13	-	
Total Gate Charge ^{2,3}	Q_g	$I_D=14\text{A}, V_{\text{DS}}=200\text{V}, V_{\text{GS}}=10\text{V}$	-	18	-	nC
Gate-to-Source Charge ^{2,3}	Q_{gs}		-	7.8	-	
Gate-to-Drain ("Miller") Charge ^{2,3}	Q_{gd}		-	7.5	-	
Turn-On Delay Time ^{2,3}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=125\text{V}, V_{\text{GS}}=10\text{V}, R_G=25\Omega, I_D=14\text{A}$	-	15	-	nS
Rise Time ^{2,3}	t_r		-	61	-	
Turn-Off Delay Time ^{2,3}	$t_{\text{d}(\text{off})}$		-	25	-	
Fall Time ^{2,3}	t_f		-	26	-	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, f=1\text{MHz}$	-	3.1	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_s	$T_C=25^\circ\text{C}$, MOSFET symbol showing the integral reverse p-n junction diode.	-	-	4.4	A
Diode Pulse Current	I_{SM}		-	-	17.6	A
Diode Forward Voltage	V_{SD}	$I_s=4.0\text{A}, V_{\text{GS}}=0\text{V}$	-	-	1.3	V
Reverse Recovery Time	T_{rr}	$I_s=4\text{A}, V_{\text{GS}}=0\text{V}, dI_F/dt=100\text{A/us}$	-	150	-	nS
Reverse Recovery Charge	Q_{rr}		-	500	-	nC

Notes:

1. Pulse width limited by maximum junction temperature.
2. Pulse test: Pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
3. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

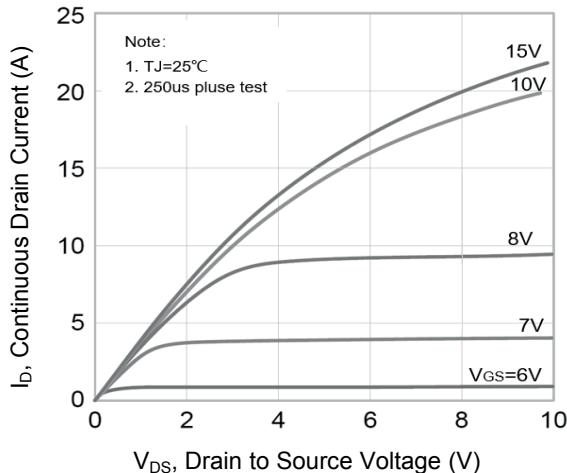


Figure 1. Output Characteristics

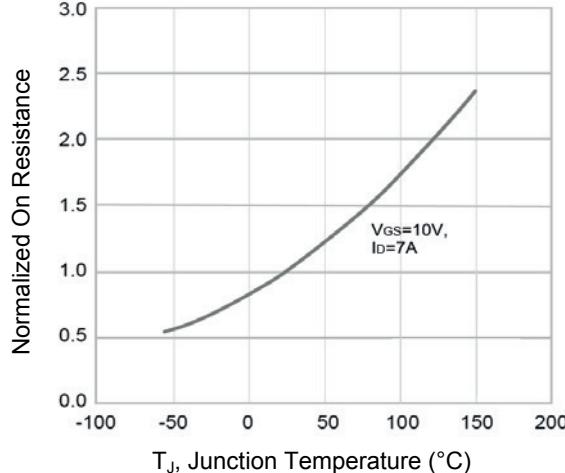


Figure 2. Normalized $R_{DS(ON)}$ vs. T_J

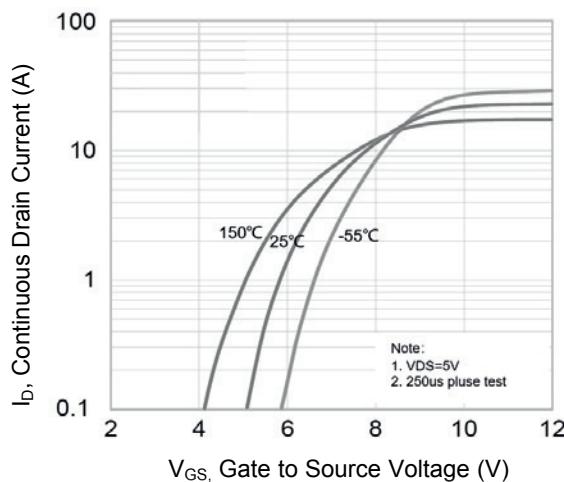


Figure 3. Transfer Characteristics

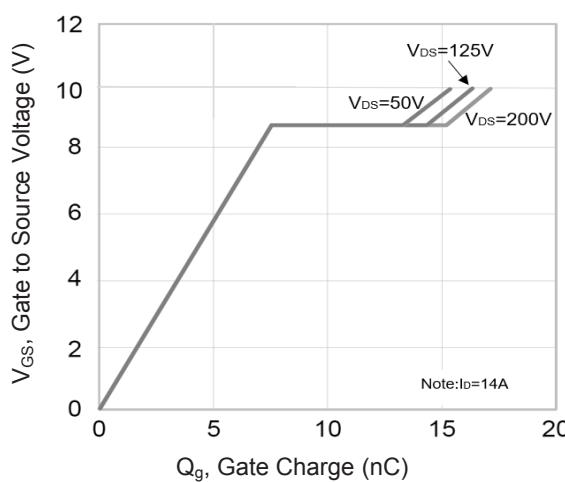


Figure 4. Gate Charge Waveform

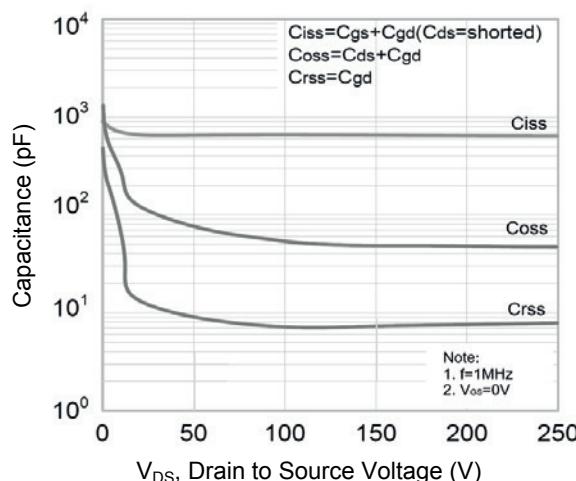


Figure 5. Capacitance Characteristics

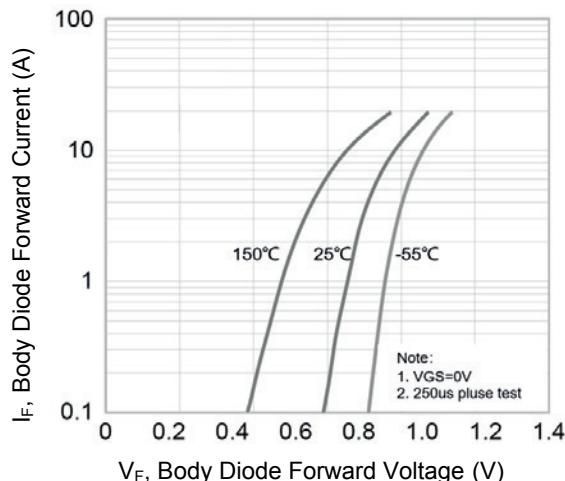
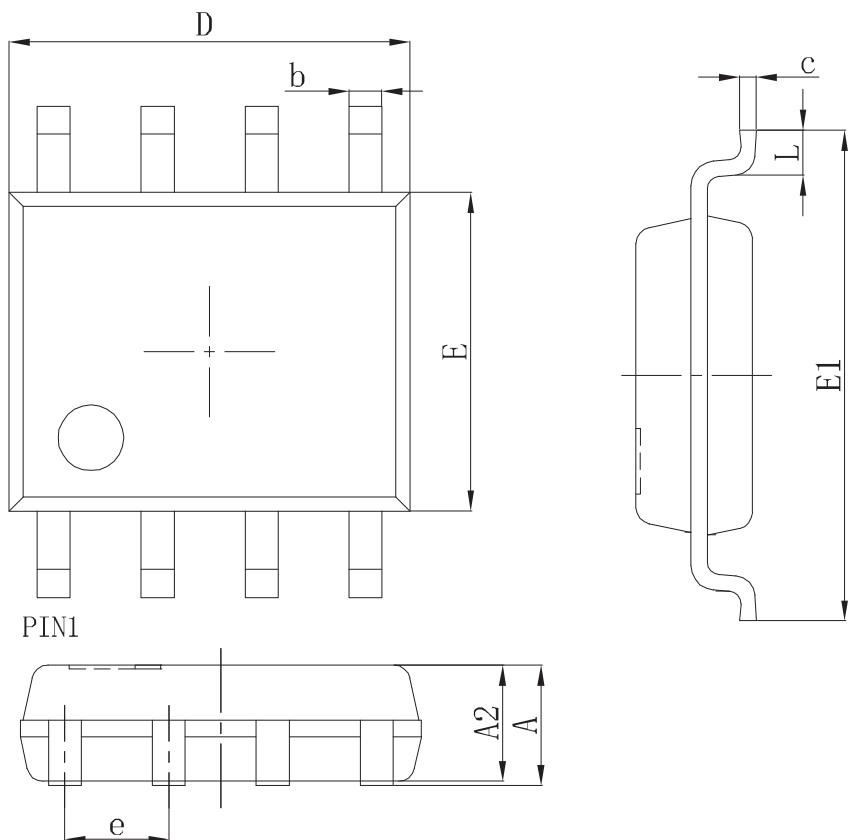


Figure 6. Body Diode Characteristics

Package Outline Dimensions (SOP-8)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	1.30	1.70	0.051	0.067
A2	1.25	1.55	0.049	0.061
c	0.17	0.25	0.007	0.010
E	3.80	4.00	0.150	0.157
E1	5.80	6.20	0.228	0.244
L	0.45	0.75	0.018	0.030
b	0.33	0.51	0.013	0.020
D	4.80	5.00	0.189	0.197
e	1.27 BSC		0.050 BSC	

Order Information

Device	Package	Marking	Carrier	Quantity
GSFQ2504	SOP-8	Q2504	Tape & Reel	3,000 Pcs / Reel