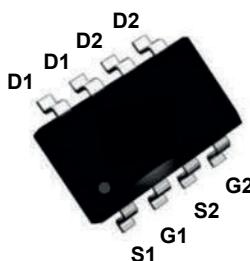
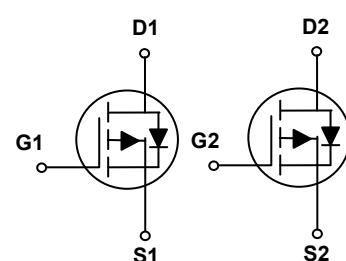


Main Product Characteristics

BV_{DSS}	-60V
$R_{DS(ON)}$	72mΩ (Typ.)
I_D	-3.6A



SOP-8



Schematic Diagram



Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

Description

The GSFQ0603 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	-60	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_A=25^\circ\text{C}$)	I_D	-3.6	A
Drain Current-Continuous ($T_A=70^\circ\text{C}$)		-2.8	A
Drain Current-Pulsed ¹	I_{DM}	-13.2	A
Single Pulse Avalanche Energy ²	E_{AS}	25	mJ
Single Pulse Avalanche Current ²	I_{AS}	-18	A
Power Dissipation ($T_A=25^\circ\text{C}$)	P_D	2	W
Power Dissipation-Derate Above 25°C		0.016	W/°C
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	62.5	°C/W
Storage Temperature Range	T_{STG}	-55 To +150	°C
Operating Junction Temperature Range	T_J	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=-250\mu\text{A}$	-60	-	-	V
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=-60\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	-1	μA
		$V_{\text{DS}}=-48\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$	-	-	-10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-2\text{A}$	-	72	96	$\text{m}\Omega$
		$V_{\text{GS}}=-4.5\text{V}, I_{\text{D}}=-1\text{A}$	-	82	130	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_{\text{D}}=-250\mu\text{A}$	-1.0	-1.8	-2.7	V
Forward Transconductance	g_{fs}	$V_{\text{DS}}=-10\text{V}, I_{\text{D}}=-1\text{A}$	-	3	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{2,3}	Q_g	$V_{\text{DS}}=-30\text{V}, I_{\text{D}}=-1\text{A}, V_{\text{GS}}=-10\text{V}$	-	10	15	nC
Gate-Source Charge ^{2,3}	Q_{gs}		-	1.6	3.2	
Gate-Drain Charge ^{2,3}	Q_{gd}		-	3	6	
Turn-On Delay Time ^{2,3}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=-30\text{V}, R_{\text{G}}=6\Omega, V_{\text{GS}}=-10\text{V}, I_{\text{D}}=-1\text{A}$	-	8	16	nS
Rise Time ^{2,3}	t_r		-	15.4	30	
Turn-Off Delay Time ^{2,3}	$t_{\text{d}(\text{off})}$		-	42.8	80	
Fall Time ^{2,3}	t_f		-	8.4	16	
Input Capacitance	C_{iss}	$V_{\text{DS}}=-30\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	720	1080	pF
Output Capacitance	C_{oss}		-	42	63	
Reverse Transfer Capacitance	C_{rss}		-	32	48	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	22	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current	I_s	$V_G=V_D=0\text{V}, \text{Force Current}$	-	-	-3.3	A
Pulsed Source Current	I_{SM}		-	-	-6.6	A
Diode Forward Voltage	V_{SD}	$V_{\text{GS}}=0\text{V}, I_{\text{s}}=-1\text{A}, T_J=25^\circ\text{C}$	-	-	-1	V
Reverse Recovery Time	T_{rr}	$V_R=-50\text{V}, I_{\text{s}}=-1\text{A}, \frac{di}{dt}=100\text{A}/\mu\text{s}, T_J=25^\circ\text{C}$	-	30	-	nS
			-	15	-	nC

Notes:

- Repetitive rating: pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=-25\text{V}, V_{\text{GS}}=-10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=-18\text{A}, R_{\text{G}}=25\Omega$, starting $T_J=25^\circ\text{C}$.
- Pluse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.

Typical Electrical and Thermal Characteristic Curves

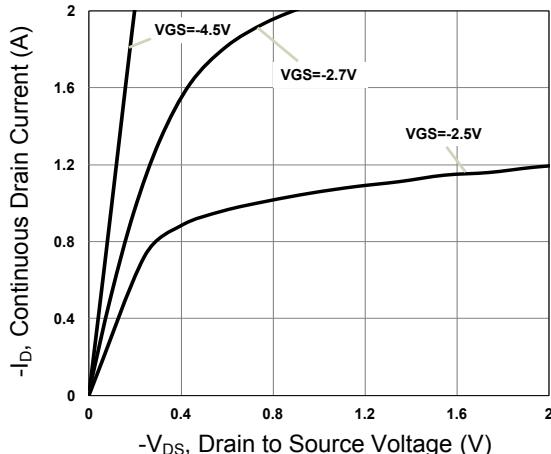


Figure 1. Typical Output Characteristics

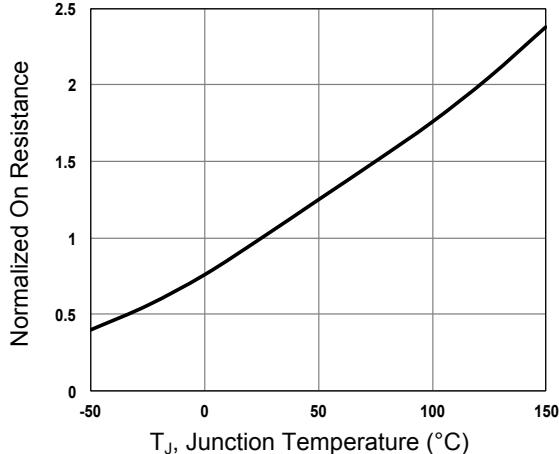


Figure 2. Normalized $R_{DS(ON)}$ vs. T_J

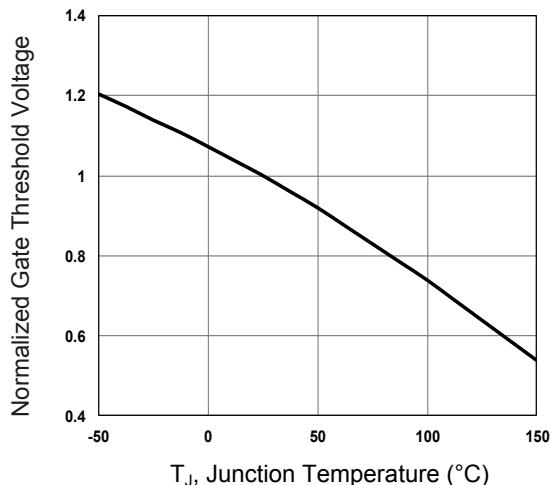


Figure 3. Normalized V_{th} vs. T_J

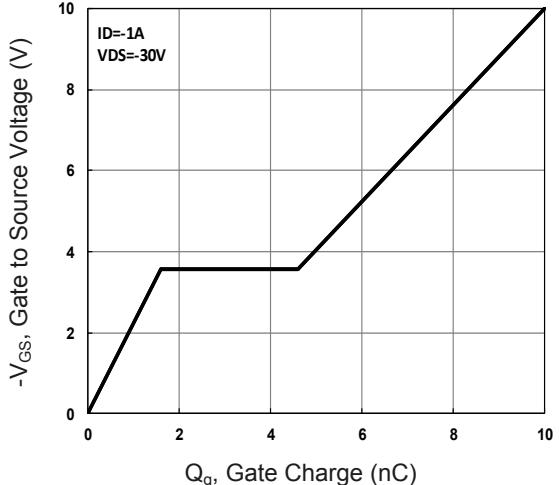


Figure 4. Gate Charge Waveform

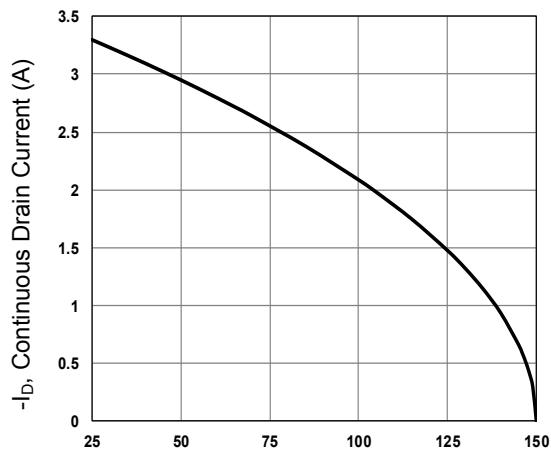


Figure 5. Continuous Drain Current vs. T_C

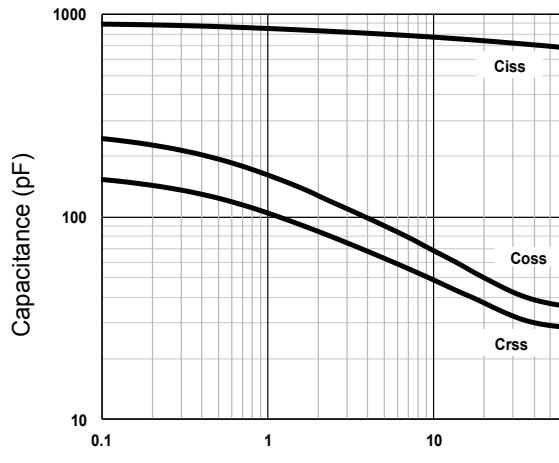


Figure 6. Capacitance Characteristics

Typical Electrical and Thermal Characteristic Curves

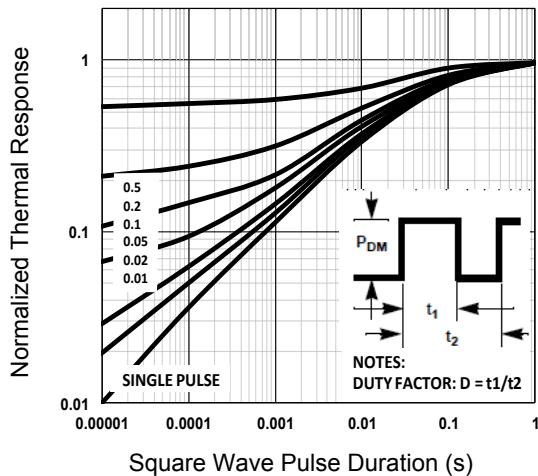


Figure 7. Normalized Transient Impedance

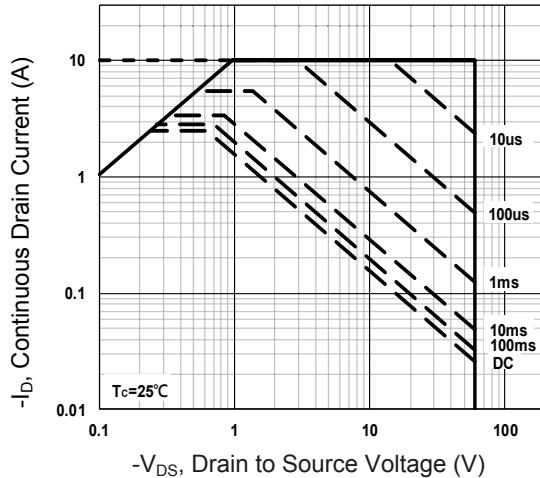
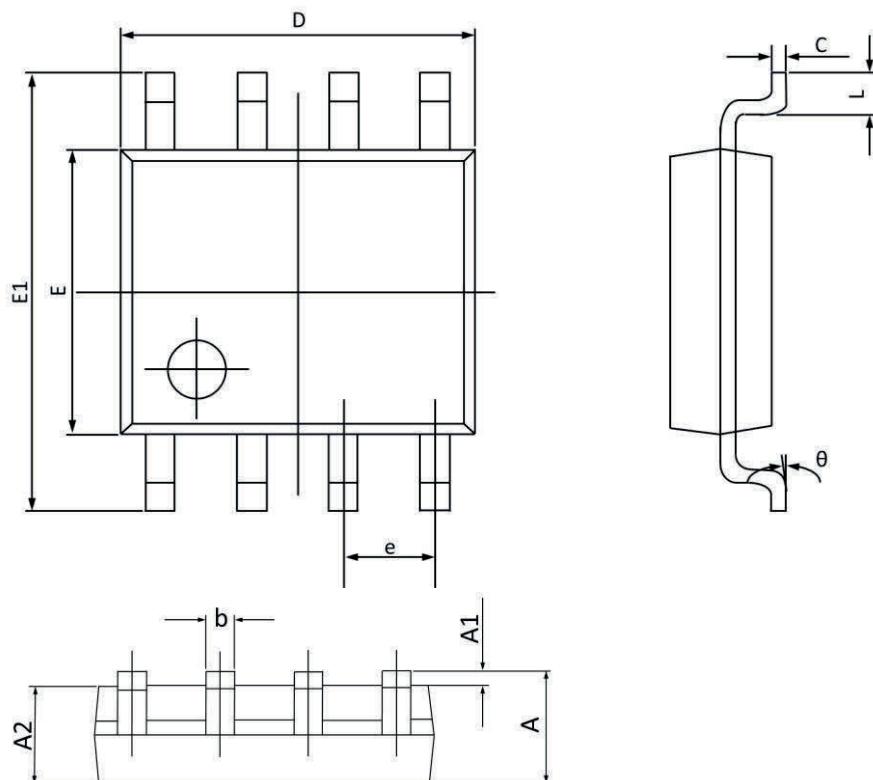


Figure 8. Maximum Safe Operation Area

Package Outline Dimensions (SOP-8)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.300	1.500	0.051	0.059
b	0.350	0.490	0.014	0.019
C	0.190	0.260	0.007	0.010
D	4.700	5.100	0.185	0.201
E	3.700	4.100	0.146	0.161
E1	5.800	6.200	0.228	0.244
e	1.27 BSC		0.05 BSC	
L	0.400	0.900	0.016	0.035
θ	0°	8°	0°	8°

Order Information

Device	Package	Marking	Carrier	Quantity
GSFQ0603	SOP-8	Q0603	Tape & Reel	3,000 Pcs / Reel

For more information, please contact us at: inquiry@goodarksemi.com