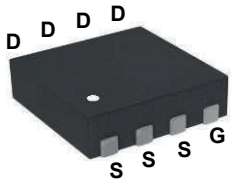
GOOD-ARK
SEMICONDUCTOR

GSFN15050

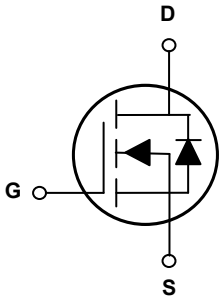
150V N-Channel MOSFET

Main Product Characteristics

$V_{(BR)DSS}$	150V
$R_{DS(ON)}$	54mΩ (Max.)
I_D	17A



DFN3x3



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery

Description

The GSFN15050 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings (T_A=25°C unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-Source Voltage	V_{GS}	±20	V
Drain Current @ T_C=25°C[5]	I_D	17	A
Drain Current @ T_C=100°C[5]		11	A
Drain Current @ T_A=25°C[5]		6	A
Drain Current @ T_A=70°C[5]		5	A
Pulsed Drain Current[2]	I_{DM}	69	A
Total Power Dissipation (T_C=25°C)[3]	P_D	39	W
Total Power Dissipation (T_A=25°C)[3]		4.2	W
Max. Thermal Resistance Junction-to-Ambient[1,4]	$R_{\theta JA}$	30	°C/W
Max. Thermal Resistance Junction-to-Case	$R_{\theta JC}$	3.2	°C/W
Junction and Storage Temperature Range	T_J, T_{STG}	-55 to +150	°C

Electrical Characteristics (T_J=25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min	Typ	Max	Units
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	V_{GS}=0V, I_D=250μA	150	-	-	V
Zero Gate Voltage Drain Current	I_{DSS}	V_{DS}=120V, V_{GS}=0V, T_C=25°C	-	-	1	μA
Gate-Body Leakage Current	I_{GSS}	V_{GS}=±20V, V_{DS}=0V	-	-	±100	nA
Gate Threshold Voltage	$V_{GS(th)}$	V_{DS}=V_{GS}, I_D=250μA	2	-	4	V
Static Drain-Source On-Resistance	$R_{DS(ON)}$	V_{GS}=10V, I_D=7.5A	-	46	54	mΩ
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	V_{DS}=75V, V_{GS}=0V, f=1MHz	-	740	-	pF
Output Capacitance	C_{oss}		-	75	-	
Reverse Transfer Capacitance	C_{rss}		-	10	-	
Total Gate Charge	Q_g	V_{GS}=10V, V_{DS}=75V, I_D=5A	-	12.5	-	nC
Gate Source Charge	Q_{gs}		-	4.4	-	
Gate Drain Charge	Q_{gd}		-	4	-	
Turn-on Delay Time	$t_{d(on)}$	V_{GS}=10V, V_{DD}=75V, R_L=15Ω, R_G=3Ω	-	7.38	-	nS
Turn-on Rise Time	t_r		-	3.59	-	
Turn-off Delay Time	$t_{d(off)}$		-	13.3	-	
Turn-off Fall Time	t_f		-	5.26	-	
Source-Drain Ratings and Characteristics						
Diode Forward Voltage	V_{SD}	I_S=7.5A, V_{GS}=0V	-	0.85	1.2	V
Maximum Body-Diode Continuous Current	I_S	-	-	-	17	A

Notes:

1. The value of $R_{\theta JA}$ is measured with the device mounted on 1 in^2 FR-4 board with 2oz.copper, in a still air environment with T_A=25°C. The power dissipation P_{DSM} is based on $R_{\theta JA}$ t ≤10s and the maximum allowed junction temperature of 150°C. The value in any given application depends on the user's specific board design.
2. Single pulse width limited by junction temperature $T_{J(MAX)}$=150°C.
3. The power dissipation P_D is based on $T_{J(MAX)}$=150°C, using junction-to-case thermal resistance, and is more useful in setting the upper dissipation limit for cases where additional heatsinking is used.
4. The $R_{\theta JA}$ is the sum of the thermal impedance from junction to case $R_{\theta JA}$ and case to ambient.
5. The maximum current rating is package limited.

Typical Electrical and Thermal Characteristic Curves

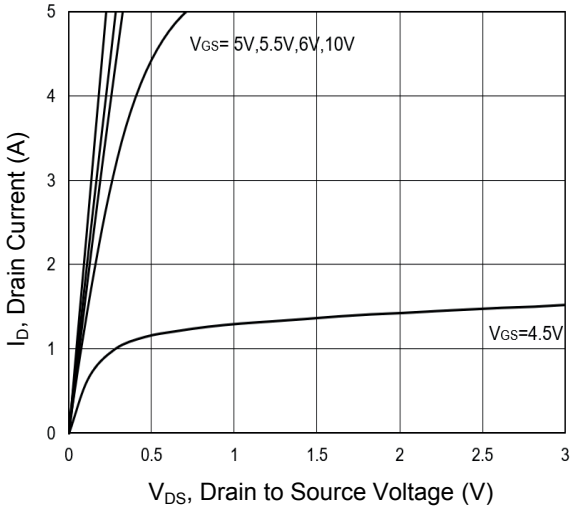


Figure 1. Output Characteristics

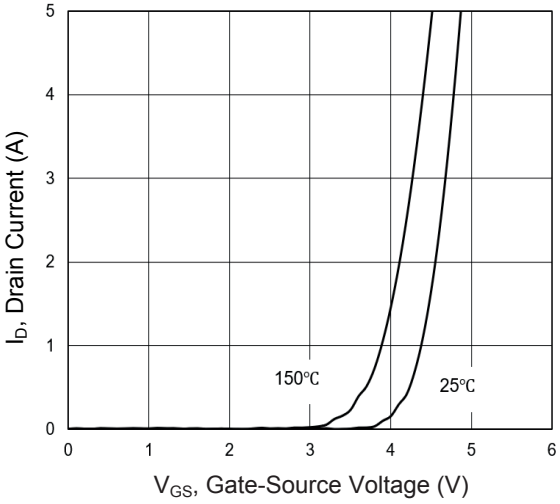


Figure 2. Transfer Characteristics

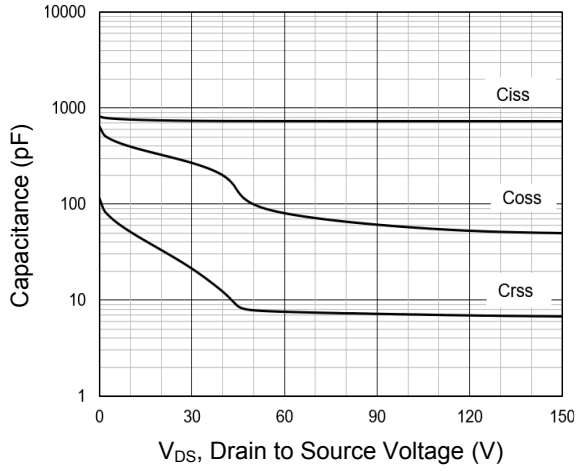


Figure 3. Capacitance Characteristics

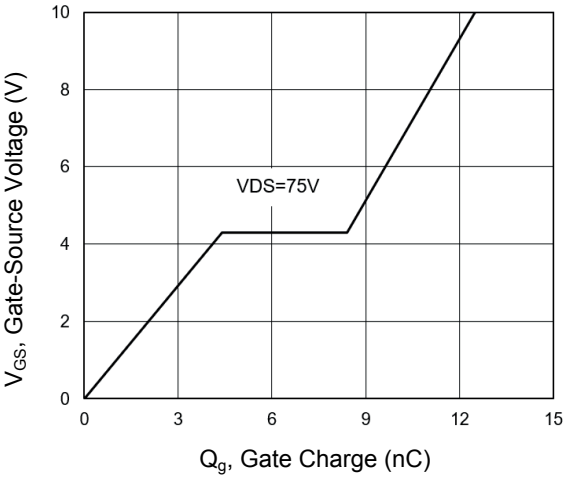


Figure 4. Gate Charge

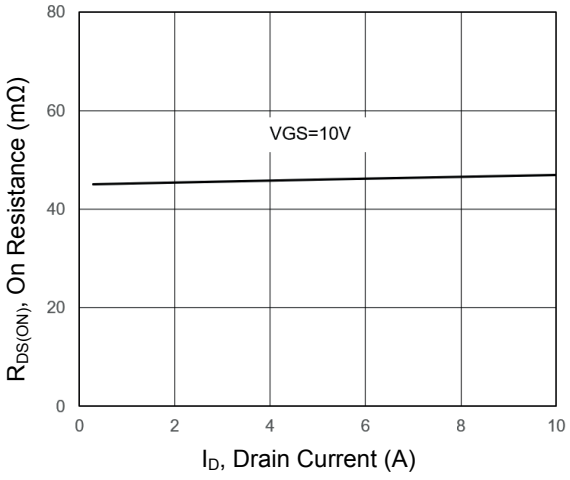


Figure 5. Drain-Source On Resistance

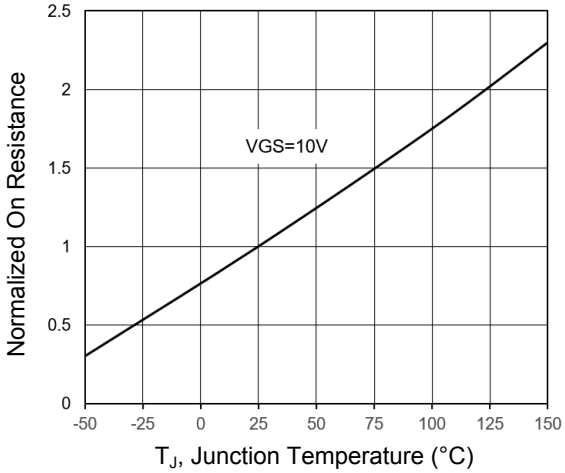


Figure 6. Normalized On-Resistance vs. T_J

Typical Electrical and Thermal Characteristic Curves

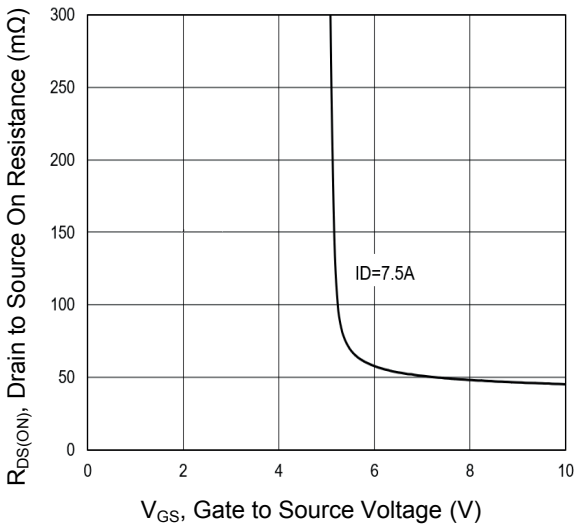


Figure 7. Typical Drain to Source On Resistance vs. Gate Voltage

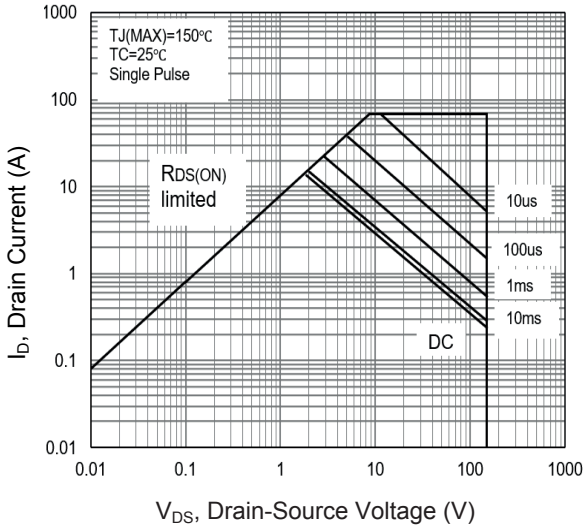


Figure 8. Safe Operation Area

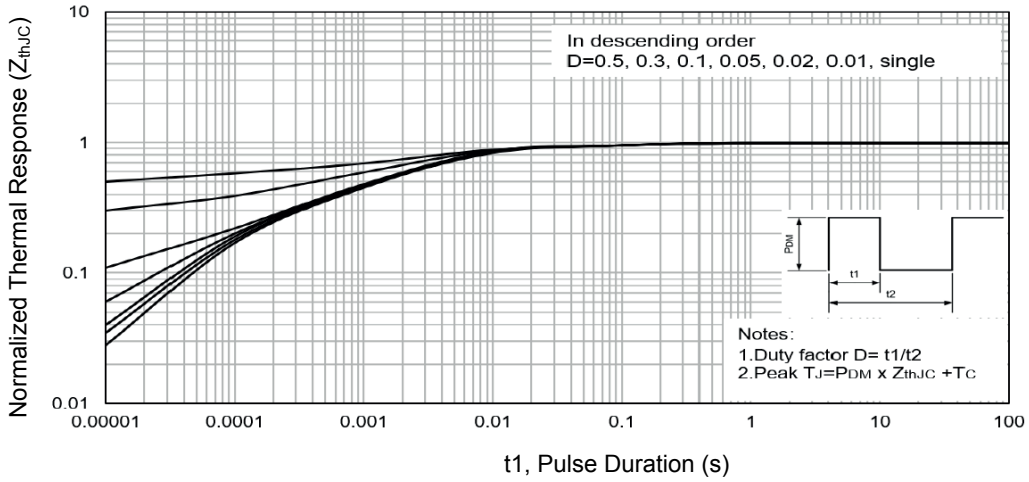
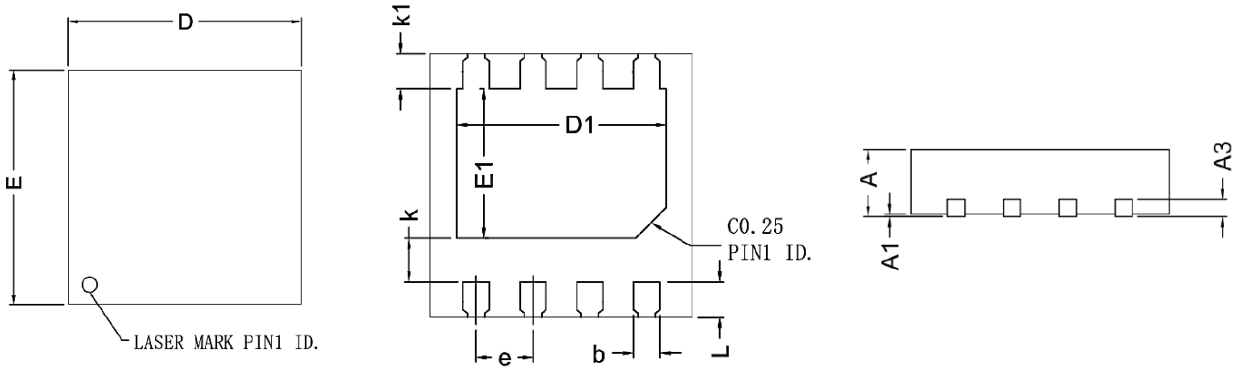


Figure 9. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Package Outline Dimensions (DFN3x3)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.700	0.800	0.028	0.031
A1	-	0.050	-	0.002
A3	0.200 REF		0.008 REF	
b	0.250	0.350	0.010	0.014
D	2.950	3.050	0.116	0.120
D1	2.350	2.450	0.093	0.096
E	2.950	3.050	0.116	0.120
E1	1.650	1.750	0.065	0.069
e	0.650 BSC		0.026 BSC	
k	0.500 REF		0.020 REF	
k1	0.400 REF		0.016 REF	
L	0.350	0.450	0.014	0.018

Order Information

Device	Package	Marking	Packaging	SPQ
GSFN15050	DFN3x3	N15050	Tape & Reel	5,000 Pcs / Reel