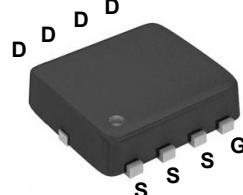
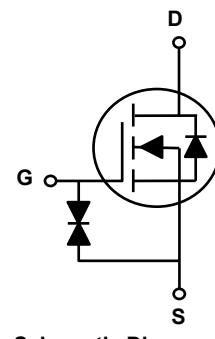


### Main Product Characteristics

BV <sub>DSS</sub>	100V
R <sub>DS(ON)</sub>	40mΩ
I <sub>D</sub>	16A



PPAK 3x3



Schematic Diagram

### Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



### Description

The GSFN1016 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

### Absolute Maximum Ratings ( $T_c=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V <sub>DS</sub>	100	V
Gate-Source Voltage	V <sub>GS</sub>	±20	V
Drain Current - Continuous ( $T_c=25^\circ\text{C}$ )	I <sub>D</sub>	16	A
Drain Current - Continuous ( $T_c=100^\circ\text{C}$ )		10	
Drain Current-Pulsed <sup>1</sup>	I <sub>DM</sub>	64	A
Power Dissipation ( $T_c=25^\circ\text{C}$ )	P <sub>D</sub>	29.5	W
Power Dissipation-Derate above 25°C		0.24	W/°C
Thermal Resistance, Junction-to-Ambient	R <sub>θJA</sub>	62	°C/W
Thermal Resistance Junction to Case	R <sub>θJC</sub>	4.24	°C/W
Operating Junction Temperature Range	T <sub>J</sub>	-55 To +150	°C
Storage Temperature Range	T <sub>STG</sub>	-55 To +150	°C


**GSFN1016**
**100V N-Channel MOSFET**
**Electrical Characteristics** ( $T_J=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On/Off Characteristics</b>						
Drain-Source Breakdown Voltage	$\text{BV}_{\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	100	-	-	V
Drain-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=80\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1	$\mu\text{A}$
		$V_{\text{DS}}=80\text{V}, V_{\text{GS}}=0\text{V}, T_J=85^\circ\text{C}$	-	-	10	$\mu\text{A}$
Gate-Source Leakage Current	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	$\pm20$	$\mu\text{A}$
Static Drain-Source On-Resistance	$R_{\text{DS(ON)}}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	-	34	40	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS(th)}}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	2.5	-	4.0	V
<b>Dynamic and Switching Characteristics</b>						
Total Gate Charge <sup>2</sup>	$Q_g$	$V_{\text{DS}}=50\text{V}, I_{\text{D}}=8\text{A}, V_{\text{GS}}=10\text{V}$	-	5.6	12	nC
Gate-Source Charge <sup>2</sup>	$Q_{gs}$		-	1.2	2.5	
Gate-Drain Charge <sup>2</sup>	$Q_{gd}$		-	1.7	3.5	
Turn-On Delay Time <sup>2</sup>	$t_{\text{d(on)}}$	$V_{\text{DD}}=50\text{V}, R_{\text{G}}=6\Omega, V_{\text{GS}}=10\text{V}, I_{\text{D}}=8\text{A}$	-	4.5	10	nS
Rise Time <sup>2</sup>	$t_r$		-	8	16	
Turn-Off Delay Time <sup>2</sup>	$t_{\text{d(off)}}$		-	12	24	
Fall Time <sup>2</sup>	$t_f$		-	10	20	
Input Capacitance <sup>2</sup>	$C_{\text{iss}}$	$V_{\text{DS}}=50\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	295	600	pF
Output Capacitance <sup>2</sup>	$C_{\text{oss}}$		-	84	170	
Reverse Transfer Capacitance <sup>2</sup>	$C_{\text{rss}}$		-	6	12	
Gate Resistance <sup>2</sup>	$R_g$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	0.4	-	$\Omega$
<b>Drain-Source Diode Characteristics and Maximum Ratings</b>						
Continuous Source Current	$I_s$	$V_G=V_D=0\text{V}, \text{Force Current}$	-	-	16	A
Pulsed Source Current	$I_{\text{SM}}$		-	-	32	A
Diode Forward Voltage	$V_{\text{SD}}$	$V_{\text{GS}}=0\text{V}, I_s=1\text{A}, T_J=25^\circ\text{C}$	-	-	1	V

Notes:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2. Essentially independent of operation temperature.

### Typical Electrical and Thermal Characteristic Curves

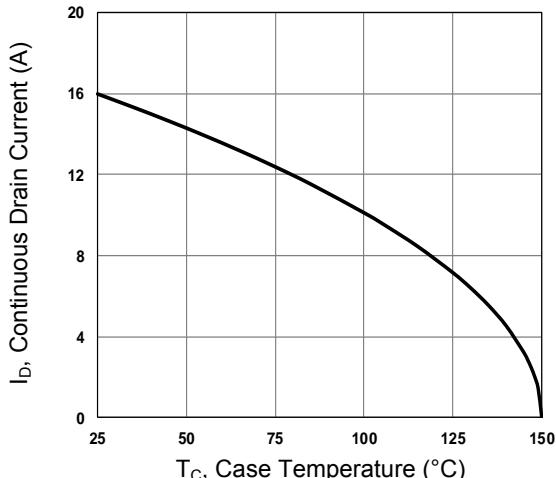


Figure 1. Continuous Drain Current vs. T<sub>c</sub>

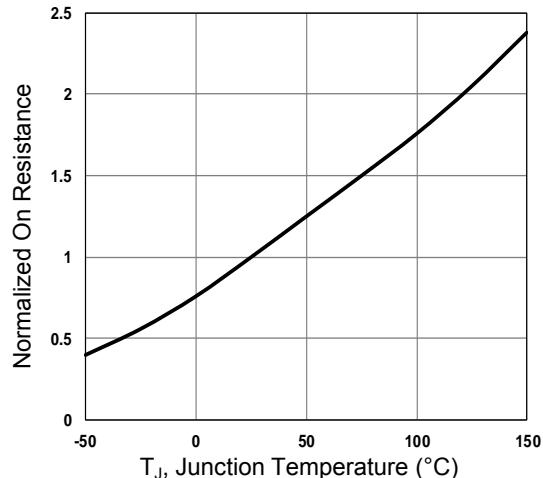


Figure 2. Normalized R<sub>DS(on)</sub> vs. T<sub>j</sub>

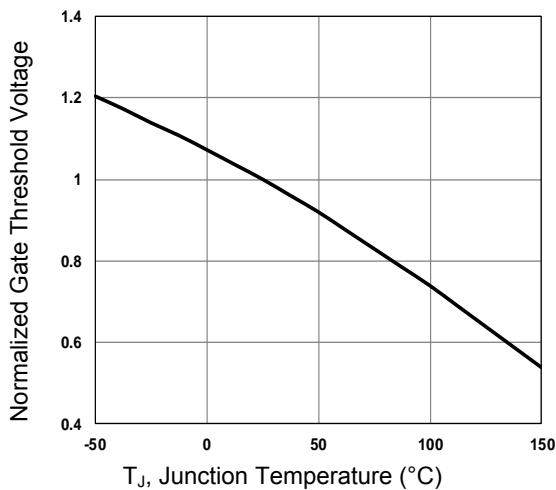


Figure 3. Normalized V<sub>th</sub> vs. T<sub>j</sub>

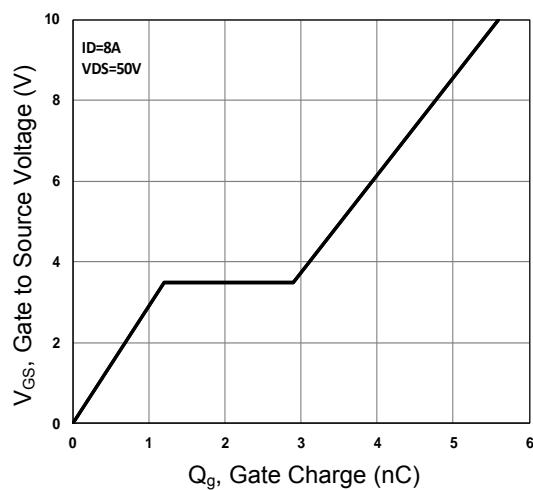


Figure 4. Gate Charge Waveform

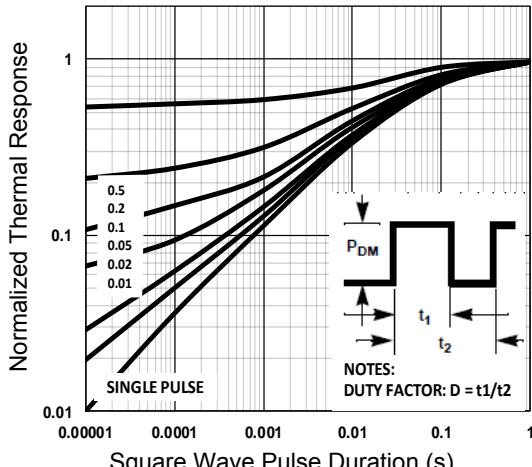


Figure 5. Normalized Transient Impedance

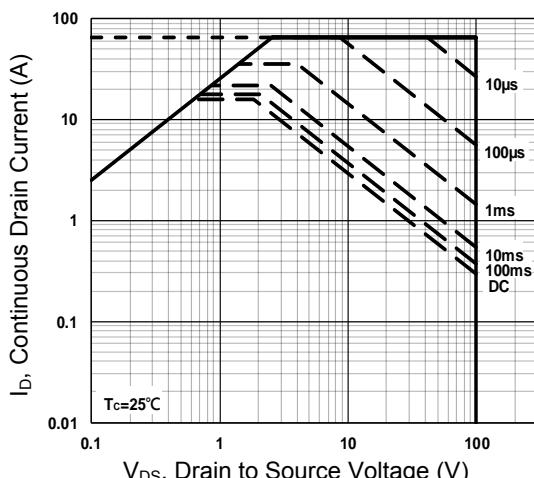
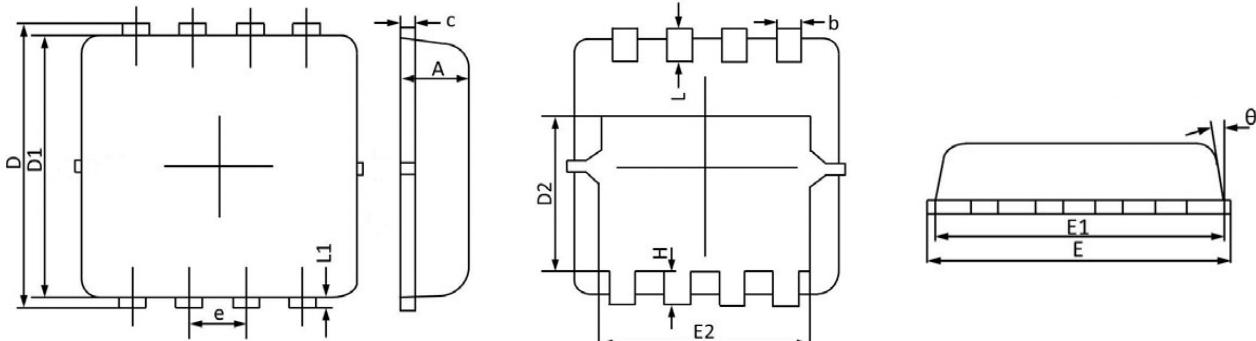


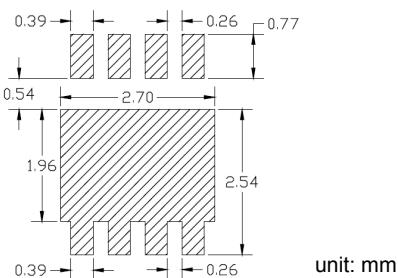
Figure 6. Maximum Safe Operation Area

### Package Outline Dimensions (PPAK3x3)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Max.	Min.	Max.	Min.
A	0.900	0.700	0.035	0.028
b	0.350	0.250	0.014	0.010
c	0.250	0.100	0.010	0.004
D	3.500	3.050	0.138	0.120
D1	3.200	2.900	0.126	0.114
D2	1.950	1.350	0.077	0.053
E	3.400	3.000	0.134	0.118
E1	3.300	2.900	0.130	0.114
E2	2.600	2.350	0.102	0.093
e	0.65BSC		0.026BSC	
H	0.750	0.300	0.030	0.012
L	0.600	0.300	0.024	0.012
L1	0.200	0.060	0.008	0.002
θ	14°	6°	14°	6°

### Recommended Pad Layout



### Order Information

Device	Package	Marking	Carrier	Quantity
GSFN1016	PPAK3x3	09F0BHZ	Tape & Reel	3,000 Pcs / Reel

For more information, please contact us at: [inquiry@goodarksemi.com](mailto:inquiry@goodarksemi.com)