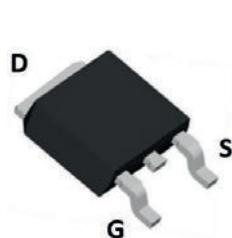
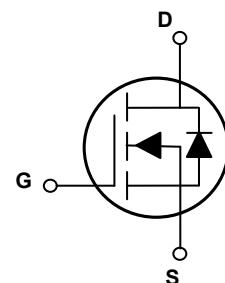


Main Product Characteristics

BV_{DSS}	40V
$R_{DS(ON)}$	7.1mΩ (Typ.)
I_D	60A



TO-252 (DPAK)



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFD4010 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	40	V
Gate-Source Voltage	V_{GS}	± 20	V
Drain Current-Continuous ($T_C=25^\circ\text{C}$)	I_D	60	A
Drain Current-Continuous ($T_C=100^\circ\text{C}$)		38	
Drain Current-Pulsed ¹	I_{DM}	240	A
Single Pulse Avalanche Energy ²	E_{AS}	76	mJ
Single Pulse Avalanche Current ²	I_{AS}	39	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	62	W
Power Dissipation-Derate above 25°C		0.496	W/°C
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	62	°C/W
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	2.01	°C/W
Operating Junction Temperature Range	T_J	-55 To +150	°C
Storage Temperature Range	T_{STG}	-55 To +150	°C

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{\text{GS}}=0\text{V}, I_{\text{D}}=250\mu\text{A}$	40	-	-	V
BV_{DSS} Temperature Coefficient	$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Reference to 25°C , $I_{\text{D}}=1\text{mA}$	-	0.03	-	$\text{V}/^\circ\text{C}$
Drain-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=40\text{V}, V_{\text{GS}}=0\text{V}, T_J=25^\circ\text{C}$	-	-	1	μA
		$V_{\text{DS}}=32\text{V}, V_{\text{GS}}=0\text{V}, T_J=125^\circ\text{C}$	-	-	10	μA
Gate-Source Leakage Current	I_{GSS}	$V_{\text{GS}}=\pm 20\text{V}, V_{\text{DS}}=0\text{V}$	-	-	± 100	nA
Static Drain-Source On-Resistance ³	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_{\text{D}}=10\text{A}$	-	7.1	10	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_{\text{D}}=5\text{A}$	-	10	14	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{GS}}=V_{\text{DS}}, I_{\text{D}}=250\mu\text{A}$	1.2	1.6	2.5	V
$V_{\text{GS}(\text{th})}$ Temperature Coefficient	$\Delta V_{\text{GS}(\text{th})}$		-	-5	-	$\text{mV}/^\circ\text{C}$
Forward Transconductance	g_{fs}	$V_{\text{DS}}=10\text{V}, I_{\text{D}}=3\text{A}$	-	16	-	S
Dynamic and Switching Characteristics						
Total Gate Charge ^{3,4}	Q_g	$V_{\text{DS}}=20\text{V}, I_{\text{D}}=10\text{A}, V_{\text{GS}}=4.5\text{V}$	-	16.2	32	nC
Gate-Source Charge ^{3,4}	Q_{gs}		-	3.85	7	
Gate-Drain Charge ^{3,4}	Q_{gd}		-	6.05	12	
Turn-On Delay Time ^{3,4}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=15\text{V}, R_{\text{G}}=6\Omega, V_{\text{GS}}=10\text{V}, I_{\text{D}}=1\text{A}$	-	13.6	25	nS
Rise Time ^{3,4}	t_r		-	2.5	5	
Turn-Off Delay Time ^{3,4}	$t_{\text{d}(\text{off})}$		-	68	120	
Fall Time ^{3,4}	t_f		-	5	10	
Input Capacitance	C_{iss}	$V_{\text{DS}}=25\text{V}, V_{\text{GS}}=0\text{V}, F=1\text{MHz}$	-	1540	2500	pF
Output Capacitance	C_{oss}		-	171	330	
Reverse Transfer Capacitance	C_{rss}		-	115	220	
Gate Resistance	R_g	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=0\text{V}, F=1\text{MHz}$	-	1.2	2.2	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current	I_s	$V_G=V_D=0\text{V}, \text{Force Current}$	-	-	60	A
Pulsed Source Current ³	I_{SM}		-	-	120	A
Diode Forward Voltage ³	V_{SD}	$V_{\text{GS}}=0\text{V}, I_s=1\text{A}, T_J=25^\circ\text{C}$	-	-	1	V

Notes:

1. Repetitive rating: Pulsed width limited by maximum junction temperature.
2. $V_{\text{DD}}=25\text{V}, V_{\text{GS}}=10\text{V}, L=0.1\text{mH}, I_{\text{AS}}=39\text{A}$, starting $T_J=25^\circ\text{C}$.
3. Pulse test: pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
4. Essentially independent of operation temperature.

Typical Electrical and Thermal Characteristic Curves

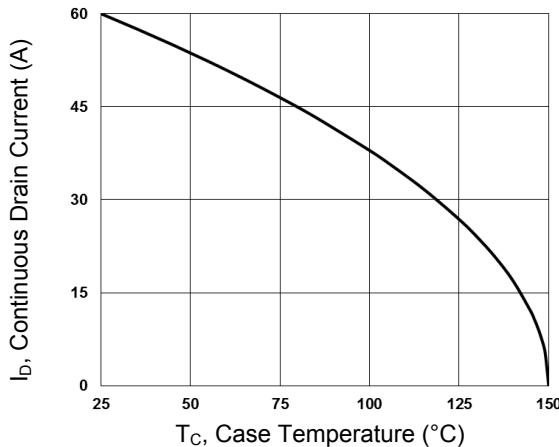


Figure 1. Continuous Drain Current vs. T_c

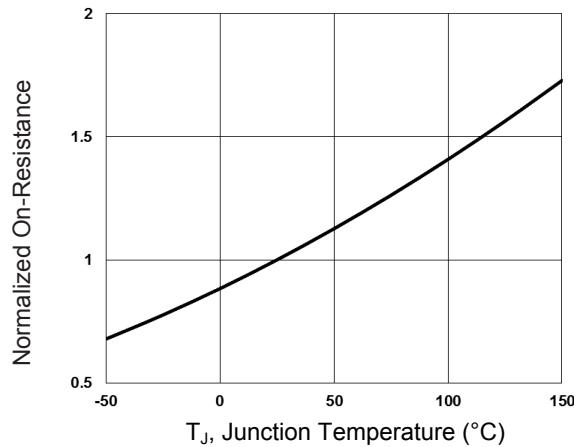


Figure 2. Normalized R_{DS(ON)} vs. T_j

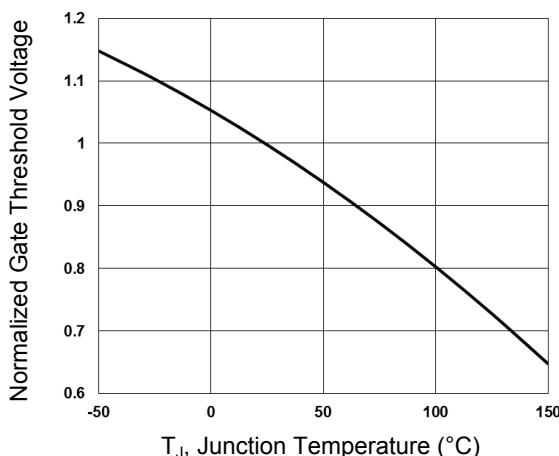


Figure 3. Normalized V_{th} vs. T_j

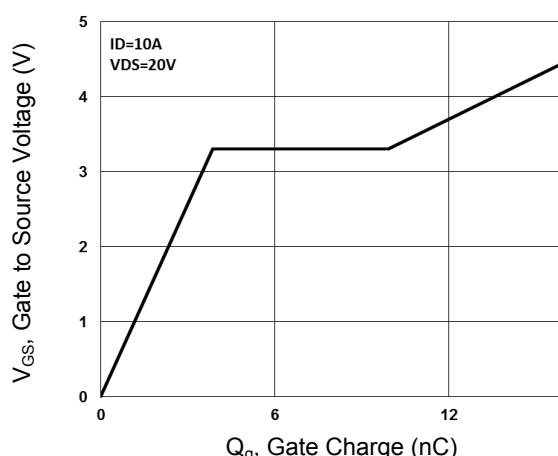


Figure 4. Gate Charge Waveform

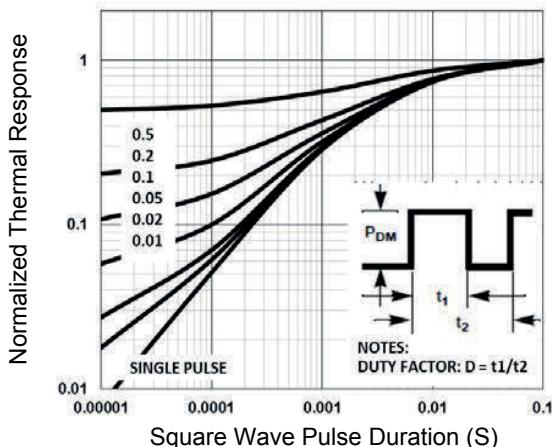


Figure 5. Normalized Transient Impedance

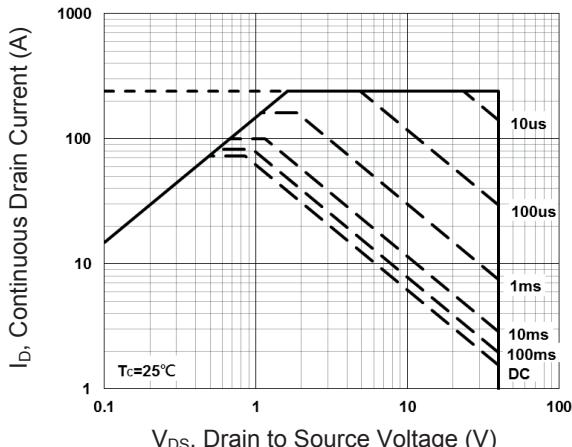
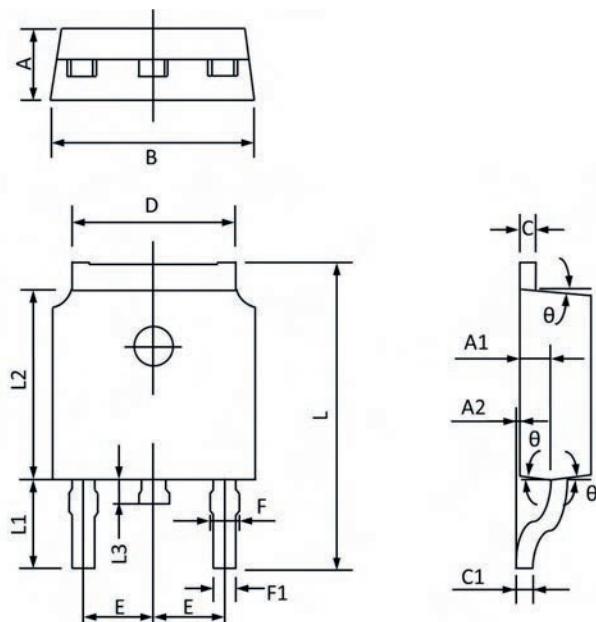


Figure 6. Maximum Safe Operation Area

Package Outline Dimensions TO-252(DPAK)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	2.200	2.400	0.087	0.094
A1	0.910	1.110	0.036	0.044
A2	0.000	0.150	0.000	0.006
B	6.400	6.800	0.252	0.268
C	0.450	0.580	0.018	0.023
C1	0.460	0.580	0.018	0.023
D	5.100	5.500	0.201	0.217
E	2.186	2.386	0.086	0.094
F	0.600	0.940	0.024	0.037
F1	0.500	0.860	0.020	0.034
L	9.400	10.400	0.370	0.409
L1	2.400	3.000	0.094	0.118
L2	5.400	6.200	0.213	0.244
L3	0.600	1.200	0.024	0.047
θ	3°	9°	3°	9°

Order Information

Device	Package	Marking	Carrier	Quantity
GSFD4010	TO-252 (DPAK)	D4010	Tape & Reel	2,500pcs / Reel

For more information, please contact us at: inquiry@goodarksemi.com