

Features

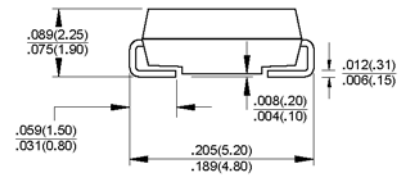
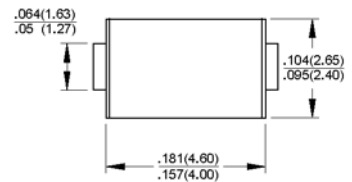
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- For surface mounted applications
- Low profile package
- Built-in strain relief, ideal for automated placement
- Glass passivated chip junction
- High temperature soldering:
250°C/10 seconds at terminals

Mechanical Data

- Case: JEDEC DO-214AC (SMA) molded plastic over glass passivated chip
- Terminals: Solder plated, solderable per MIL-STD-750, Method 2026
- Polarity: Color band denotes cathode end
- Weight: 0.002 ounce, 0.064 gram



Package: DO-214AC (SMA)



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

(T_A=25°C unless otherwise specified)

Parameter	Symbols	GN1P	Units
Maximum Repetitive Peak Reverse Voltage	V _{RRM}	1300	V
Maximum RMS Voltage	V _{RMS}	910	V
Maximum DC Blocking Voltage	V _{DC}	1300	V
Maximum Average Forward Rectified Current	I _{F(AV)}	1.0	A
Peak Forward Surge Current (8.3 ms single half sine-wave superimposed on rated load) @ T _J =110°C	I _{FSM}	30.0	A
Maximum Instantaneous Forward Voltage @1A	V _F	1.10	V
Maximum DC Reverse Current at rated DC Blocking Voltage @ T _A =25°C @ T _A =125°C	I _R	5.0 50	uA
Typical Reverse Recovery Time I _F =0.5A, I _R =1.0A I _{rr} =0.25A	t _{rr}	1.0	us
Typical Junction Capacitance @4.0 V, 1 MHz	C _J	12	pF
Typical Thermal Resistance ¹⁾	R _{θJA}	85	°C/W
	R _{θJL}	30	
Operating Junction Temperature	T _J	-55 to +150	°C
Storage Temperature	T _{STG}	-55 to +150	°C

Notes: 1. Thermal resistance from junction to ambient and from junction to lead mounted on P.C.B. with 0.2 x 0.2" (5.0 x 5.0mm) copper pad areas

Ratings and Characteristic Curves

Fig. 1 – Forward Current Derating Curve

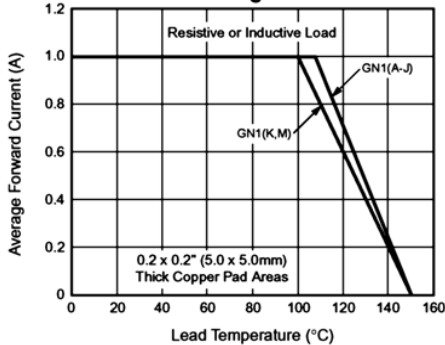


Fig. 2 – Maximum Non-Repetitive Peak Forward Surge Current

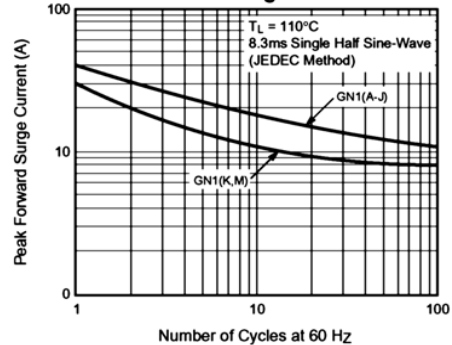


Fig. 3 – Typical Instantaneous Forward Characteristics

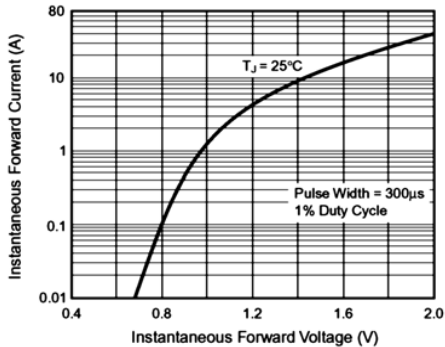


Fig. 4 – Typical Reverse Leakage Characteristics

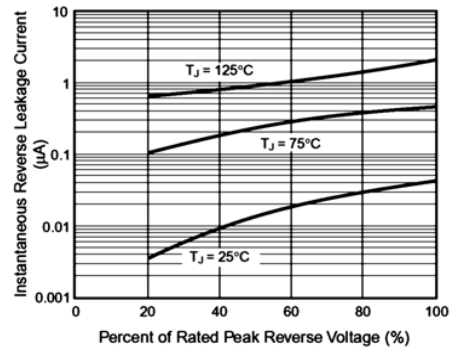


Fig. 5 – Typical Junction Capacitance

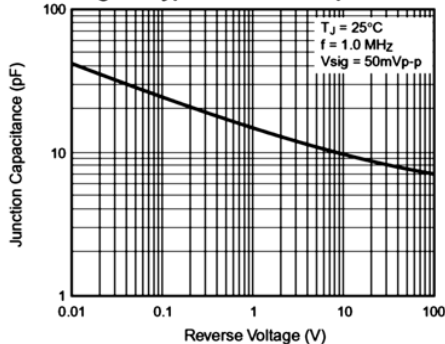


Fig. 6 – Transient Thermal Impedance

