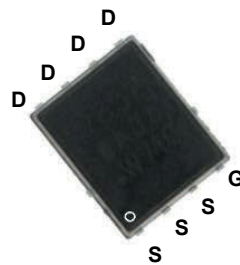
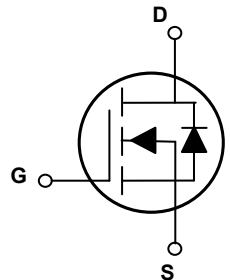


Main Product Characteristics

$V_{(BR)DSS}$	650V
$R_{DS(ON)}$	0.95Ω (Max)
I_D	5A



PPAK5x6



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery
- AEC-Q101 qualified



Description

The GSGP65R950AU utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	650	V
Gate-to-Source Voltage	V_{GS}	±30	V
Continuous Drain Current, @ Steady-State ($T_C=25^{\circ}\text{C}$)	I_D	5	A
Continuous Drain Current, @ Steady-State ($T_C=100^{\circ}\text{C}$)		3.2	A
Pulsed Drain Current	I_{DM}	20	A
Power Dissipation ($T_C=25^{\circ}\text{C}$)	P_D	80	W
		0.54	W/°C
Single Pulse Avalanche Energy ¹	E_{AS}	214	mJ
Single Pulse Avalanche Current	I_{AS}	2.8	A
Body Diode Reverse Voltage Slope ²	dv/dt	15	V/ns
MOS dv/dt Ruggedness ³	dv/dt	50	V/ns
Thermal Resistance Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62.0	°C/W
Thermal Resistance Junction-to-Case	$R_{\theta JC}$	1.88	°C/W
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +175	°C
Soldering Temperature	T_{sold}	260	°C

Electrical Characteristics ($T_J=25^{\circ}\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS}=0V, I_D=250\mu A$	650	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{DS}=650V, V_{GS}=0V, T_J=25^{\circ}\text{C}$	-	-	1.0	μA
		$V_{DS}=650V, V_{GS}=0V, T_J=125^{\circ}\text{C}$	-	1.5	-	μA
Gate-to-Source Forward Leakage	I_{GSS}	$V_{DS}=0V, V_{GS}=30V$	-	-	100	nA
		$V_{DS}=0V, V_{GS}=-30V$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=2.5A$	-	0.81	0.95	Ω
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS}=V_{GS}, I_D=250\mu A$	2.0	-	4.0	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{GS}=0V, V_{DS}=100V, f=1\text{MHz}$	-	300	-	pF
Output Capacitance	C_{oss}		-	20	-	
Reverse Transfer Capacitance	C_{rss}		-	2.4	-	
Total Gate Charge ^{4,5}	Q_g	$I_D=5A, V_{DD}=520V, V_{GS}=10V$	-	13	-	nC
Gate-to-Source Charge ^{4,5}	Q_{gs}		-	3.0	-	
Gate-to-Drain ("Miller") Charge ^{4,5}	Q_{gd}		-	6.8	-	
Gate Plateau ^{4,5}	$V_{plateau}$		-	6.5	-	V
Turn-on Delay Time ^{4,5}	$t_{d(on)}$	$V_{DD}=325V, V_{GS}=10V, R_G=24\Omega, I_D=5A$	-	8.7	-	nS
Rise Time ^{4,5}	t_r		-	25	-	
Turn-Off Delay Time ^{4,5}	$t_{d(off)}$		-	30	-	
Fall Time ^{4,5}	t_f		-	23	-	
Gate Resistance	R_g	$f=1\text{MHz}$	-	7.2	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_S	$T_C=25^{\circ}\text{C}$, MOSFET symbol showing the integral reverse p-n junction diode.	-	-	5	A
Diode Pulse Current	$I_{S,pulse}$		-	-	20	A
Diode Forward Voltage	V_{SD}	$I_S=5A, V_{GS}=0V$	-	-	1.4	V
Reverse Recovery Time ⁴	T_{rr}	$I_S=5A, V_{GS}=0V, di_F/dt=100A/\mu s$	-	334	-	nS
Reverse Recovery Charge ⁴	Q_{rr}		-	2.2	-	μC

Notes:

1. $L=79\text{mH}$, $V_{DD}=100V$, $R_G=25\Omega$, starting temperature $T_J=25^{\circ}\text{C}$.
2. $V_{DS}=0-400V$, $I_{SD}\leq I_S$, $T_J=25^{\circ}\text{C}$.
3. $V_{DS}=0-480V$.
4. Pulse test: Pulse width $\leq 300\mu s$, duty cycle $\leq 2\%$.
5. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

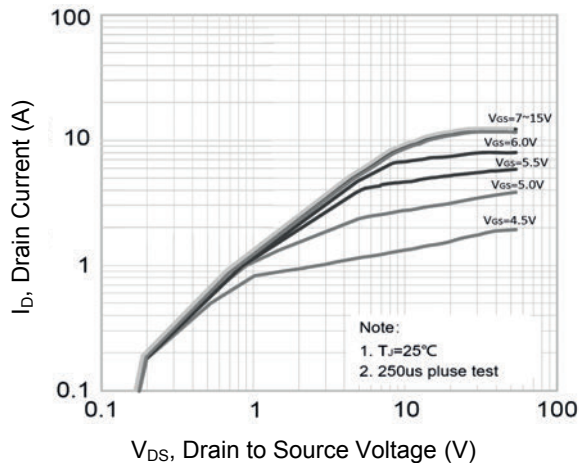


Figure 1. Typical Output Characteristics

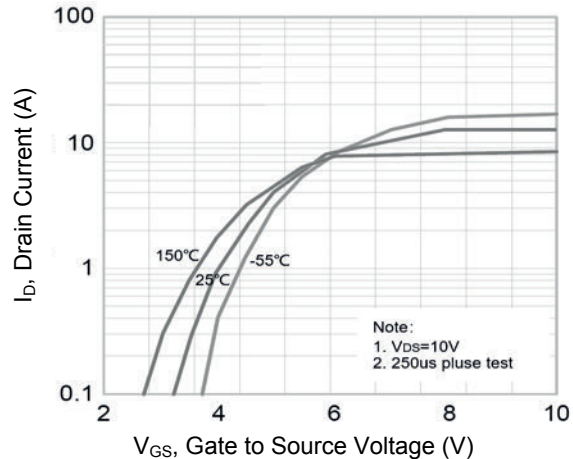


Figure 2. Transfer Characteristics

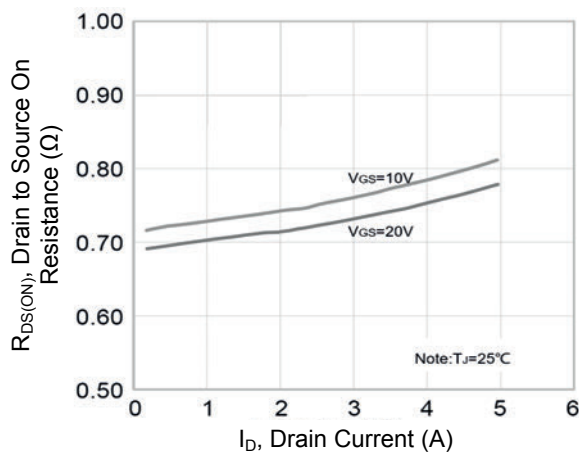


Figure 3. $R_{DS(ON)}$ vs. Drain Current

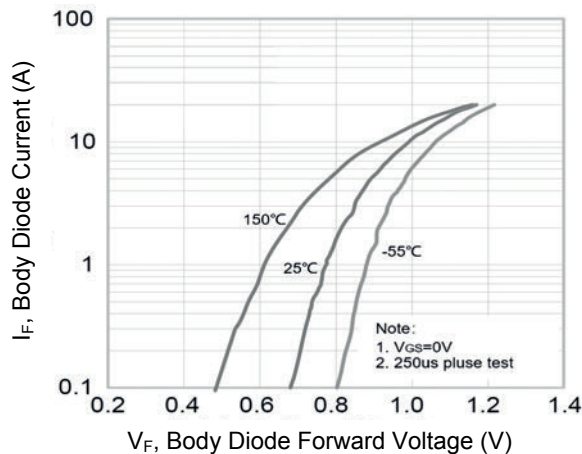


Figure 4. Body Diode Characteristics

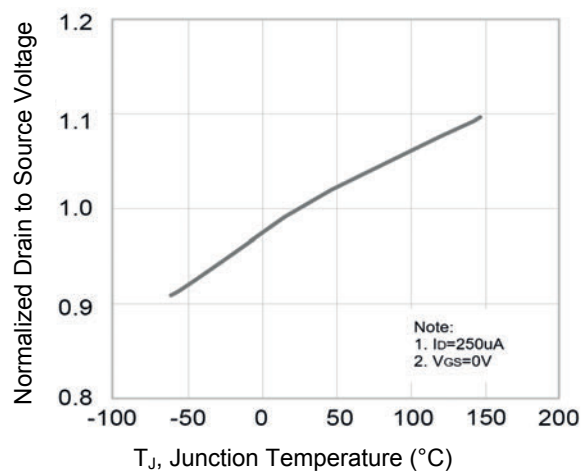


Figure 5. Normalized BV_{DSS} vs. T_J

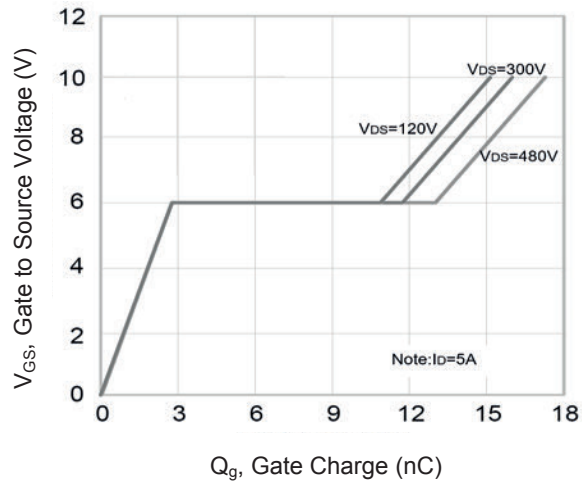


Figure 6. Gate Charge Characteristics

Typical Electrical and Thermal Characteristic Curves

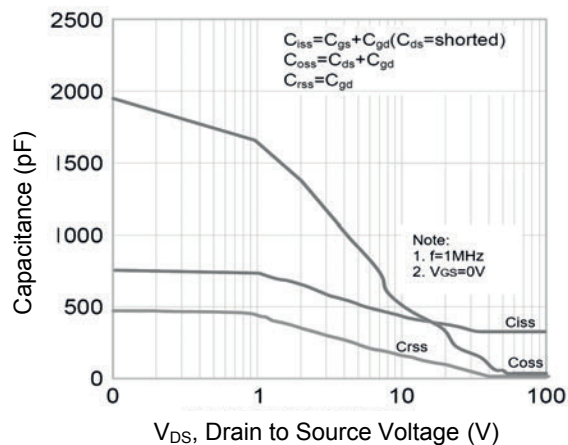


Figure 7. Capacitance Characteristics

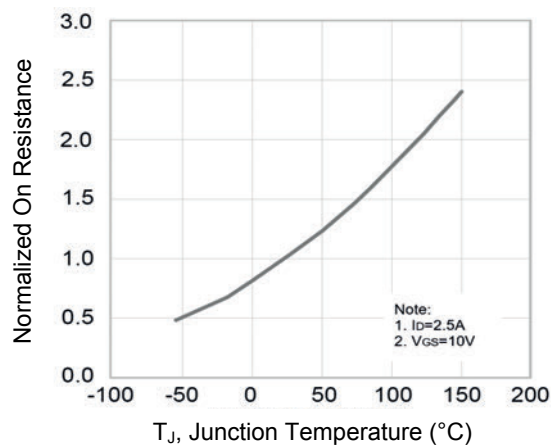


Figure 8. Normalized $R_{DS(ON)}$ vs. T_J

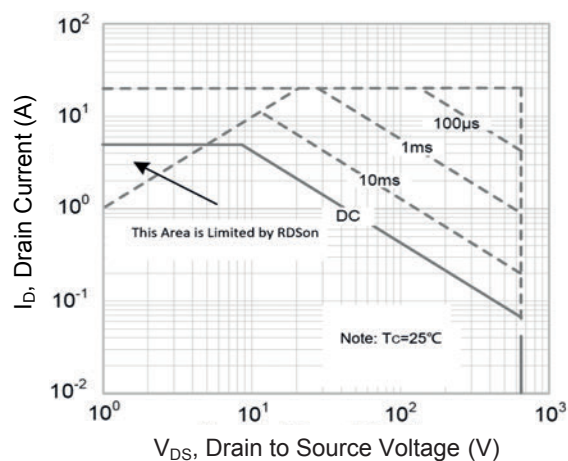
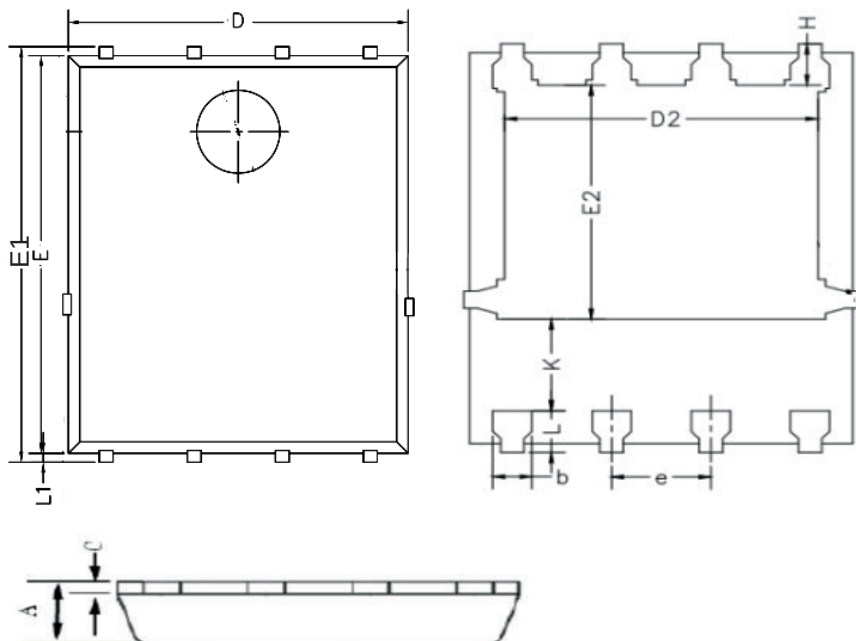


Figure 10. Safe Operation Area

Package Outline Dimensions (PPAK5x6)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min.	Max.	Min.	Max.
A	0.90	1.20	0.035	0.047
b	0.30	0.55	0.012	0.022
C	0.15	0.35	0.006	0.014
D	4.70	5.20	0.185	0.205
D2	3.76	4.20	0.148	0.165
E2	3.30	3.85	0.130	0.152
E	5.60	5.90	0.220	0.232
E1	5.80	6.20	0.228	0.244
K	1.10	-	0.043	-
H	0.45	0.75	0.018	0.030
L	0.45	0.75	0.018	0.030
L1	0.25	0.45	0.010	0.018
e	1.27 BSC		0.050 BSC	

Order Information

Device	Package	Marking	Carrier	Quantity
GSGP65R950AU	PPAK5x6	P65R950AU	Tape & Reel	5,000pcs / Reel