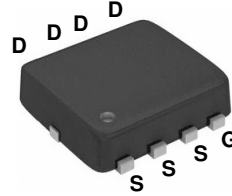
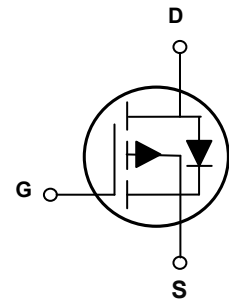


Main Product Characteristics

| | |
|--------------|----------------------|
| BV_{DSS} | -100V |
| $R_{DS(ON)}$ | 151m Ω (Max.) |
| I_D | -14A |



PPAK3x3



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFN1003 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Max. | Unit |
|---|-----------------|-------------|---------------------------|
| Drain-Source Voltage | V_{DS} | -100 | V |
| Gate-Source Voltage | V_{GS} | ± 20 | V |
| Drain Current-Continuous ($T_A=25^\circ\text{C}$) | I_D | -14 | A |
| Drain Current-Continuous ($T_A=70^\circ\text{C}$) | | -10 | |
| Drain Current-Pulsed ¹ | I_{DM} | -56 | A |
| Single Pulse Avalanche Energy ² | E_{AS} | 24 | mJ |
| Single Pulse Avalanche Current ² | I_{AS} | 22 | A |
| Power Dissipation ($T_A=25^\circ\text{C}$) | P_D | 30 | W |
| Power Dissipation - Derate above 25°C | | 0.24 | |
| Thermal Resistance, Junction-to-Case | $R_{\theta JC}$ | 4.17 | $^\circ\text{C}/\text{W}$ |
| Operating Junction Temperature Range | T_J | -55 To +150 | $^\circ\text{C}$ |
| Storage Temperature Range | T_{STG} | -55 To +150 | $^\circ\text{C}$ |

Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise specified)

| Parameter | Symbol | Conditions | Min. | Typ. | Max. | Unit |
|---|--------------|---|------|------|-----------|------------|
| On / Off Characteristics | | | | | | |
| Drain-Source Breakdown Voltage | BV_{DSS} | $V_{GS}=0V, I_D=-250\mu A$ | -100 | - | - | V |
| Drain-Source Leakage Current | I_{DSS} | $V_{DS}=-100V, V_{GS}=0V$ $T_J=25^\circ\text{C}$ | - | - | -1 | μA |
| | | $V_{DS}=-80V, V_{GS}=0V,$ $T_J=125^\circ\text{C}$ | - | - | -10 | μA |
| Gate-Source Leakage Current | I_{GSS} | $V_{GS}=\pm 20V, V_{DS}=0V$ | - | - | ± 100 | nA |
| Static Drain-Source On-Resistance | $R_{DS(ON)}$ | $V_{GS}=-10V, I_D=-5A$ | - | 125 | 151 | m Ω |
| | | $V_{GS}=-4.5V, I_D=-2A$ | - | 150 | 181 | m Ω |
| Gate Threshold Voltage | $V_{GS(th)}$ | $V_{GS}=V_{DS}, I_D=-250\mu A$ | -1.1 | -1.6 | -2.7 | V |
| Forward Transconductance | g_{fs} | $V_{DS}=-10V, I_D=-1.5A$ | - | 6.5 | - | S |
| Dynamic and Switching Characteristics | | | | | | |
| Total Gate Charge ^{3,4} | Q_g | $V_{DS}=-50V, I_D=-1A$ $V_{GS}=-10V$ | - | 20 | 30 | nC |
| Gate-Source Charge ^{3,4} | Q_{gs} | | - | 2.4 | 5 | |
| Gate-to-Drain Charge ^{3,4} | Q_{gd} | | - | 3.3 | 7 | |
| Turn-On Delay Time ^{3,4} | $t_{d(on)}$ | $V_{DD}=-50V, R_G=6\Omega$ $V_{GS}=-10V, I_D=-1A$ | - | 18 | 27 | nS |
| Rise Time ^{3,4} | t_r | | - | 8 | 12 | |
| Turn-Off Delay Time ^{3,4} | $t_{d(off)}$ | | - | 100 | 150 | |
| Fall Time ^{3,4} | t_f | | - | 30 | 45 | |
| Input Capacitance | C_{iss} | $V_{DS}=-50V, V_{GS}=0V,$ $F=1\text{MHz}$ | - | 1280 | 2000 | pF |
| Output Capacitance | C_{oss} | | - | 55 | 100 | |
| Reverse Transfer Capacitance | C_{rss} | | - | 30 | 60 | |
| Gate Resitance | R_g | $V_{GS}=0V, V_{DS}=0V,$ $F=1\text{MHz}$ | - | 16 | - | Ω |
| Drain-Source Diode Characteristics and Maximum Ratings | | | | | | |
| Continuous Source Current | I_S | $V_G=V_D=0V,$ Force Current | - | - | -15 | A |
| Pulsed Source Current | I_{SM} | | - | - | -60 | A |
| Diode Forward Voltage | V_{SD} | $V_{GS}=0V, I_S=-5A$ $T_J=25^\circ\text{C}$ | - | - | -1 | V |
| Reverse Recovery Time | T_{rr} | $V_R=-100V, I_S=-1A$ $di/dt=100A/\mu s,$ $T_J=25^\circ\text{C}$ | - | 35 | - | nS |
| Reverse Recovery Charge | Q_{rr} | | - | 30 | - | nC |

Note:

- 1.Repetitive rating: Pulsed width limited by maximum junction temperature.
2. $V_{DD}=25V, V_{GS}=10V, L=0.1\text{mH}, I_{AS}=22A,$ starting $T_J=25^\circ\text{C}.$
- 3.Pulse test: pulse width $\leq 300\mu s,$ duty cycle $\leq 2\%.$
4. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

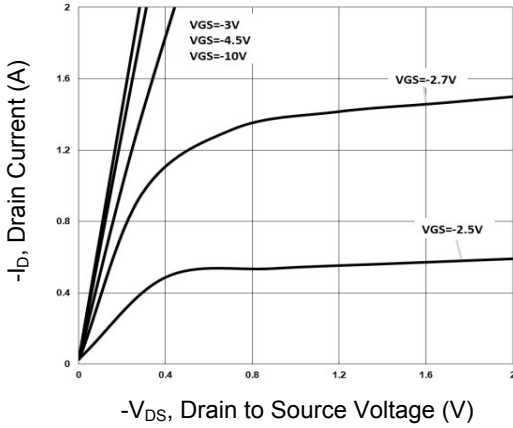


Figure 1. Output Characteristics

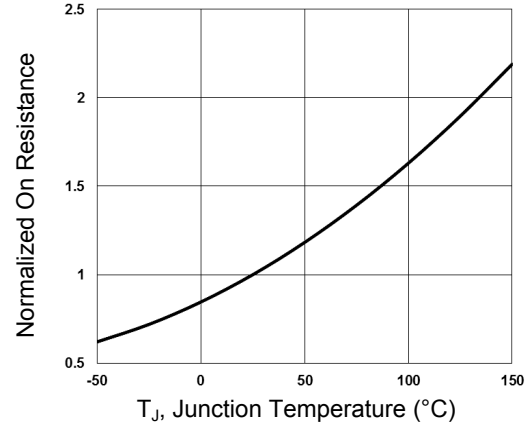


Figure 2. Normalized $R_{DS(on)}$ vs. T_J

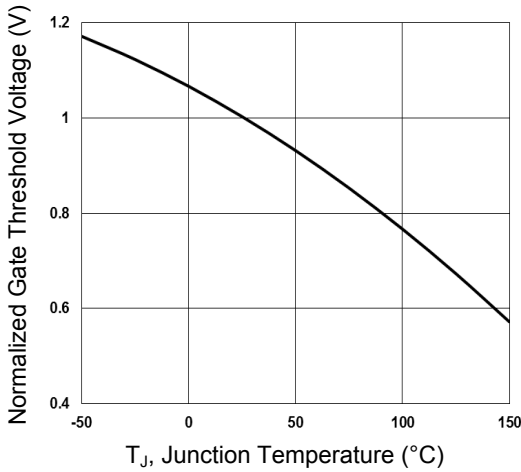


Figure 3. Normalized V_{th} vs. T_J

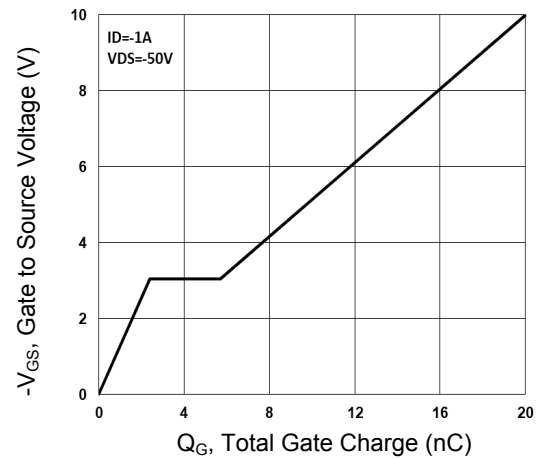


Figure 4. Gate Charge Characteristics

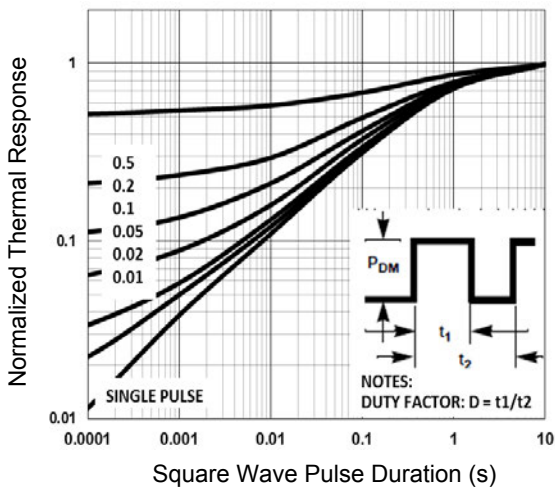


Figure 5. Normalized Transient Impedance

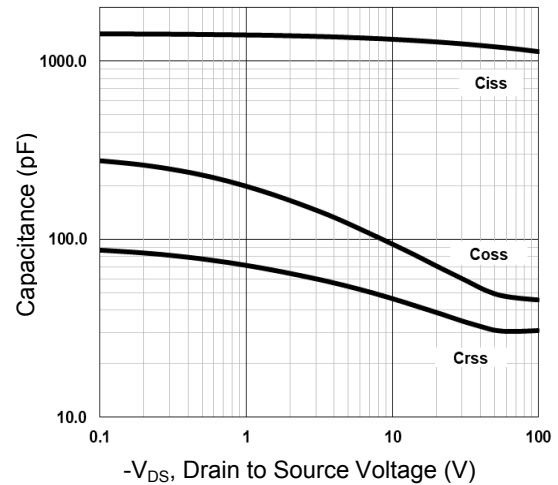


Figure 6. Capacitance Characteristics

Typical Electrical and Thermal Characteristic Curves

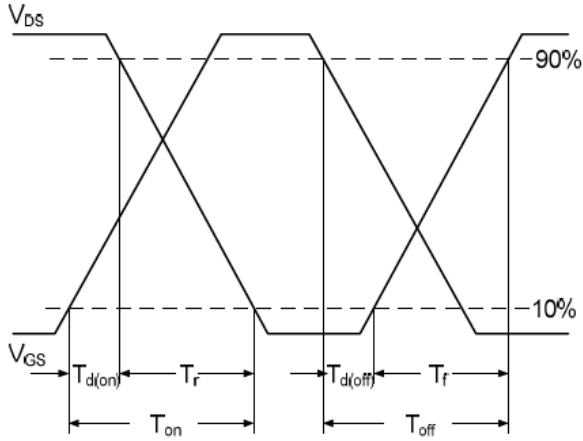


Figure 7. Switching Time Waveform

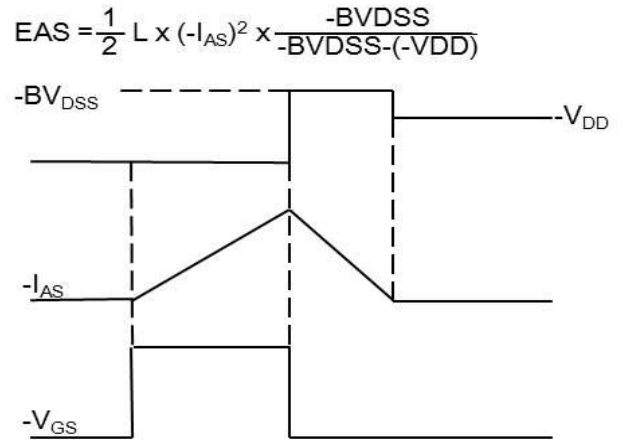
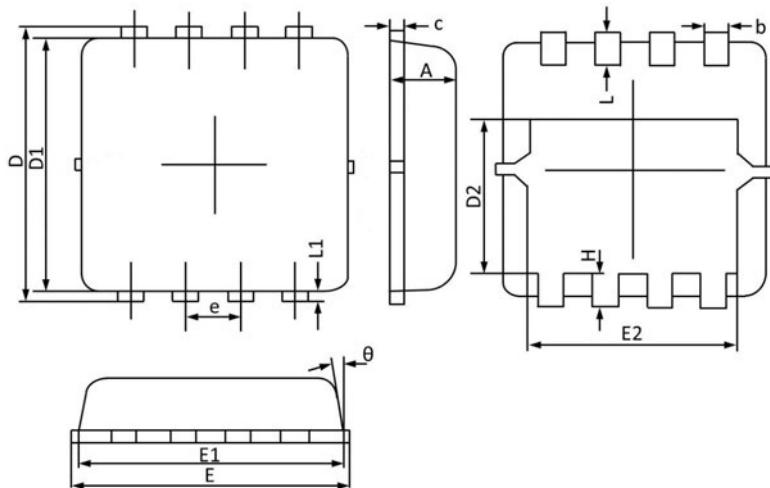


Figure 8. EAS Waveform

Package Outline Dimensions (PPAK3x3)



| Symbol | Dimensions in Millimeters | | Dimensions in Inches | |
|--------|---------------------------|-------|----------------------|-------|
| | Min | Max | Min | Max |
| A | 0.700 | 0.900 | 0.028 | 0.035 |
| b | 0.250 | 0.350 | 0.010 | 0.014 |
| c | 0.100 | 0.250 | 0.004 | 0.010 |
| D | 3.050 | 3.500 | 0.120 | 0.138 |
| D1 | 2.900 | 3.200 | 0.114 | 0.126 |
| D2 | 1.350 | 1.950 | 0.053 | 0.077 |
| E | 3.000 | 3.400 | 0.118 | 0.134 |
| E1 | 2.900 | 3.300 | 0.114 | 0.130 |
| E2 | 2.350 | 2.600 | 0.093 | 0.102 |
| e | 0.650 BSC | | 0.026 BSC | |
| H | 0.300 | 0.750 | 0.012 | 0.030 |
| L | 0.300 | 0.600 | 0.012 | 0.024 |
| L1 | 0.060 | 0.200 | 0.002 | 0.008 |
| θ | 6° | 14° | 6° | 14° |