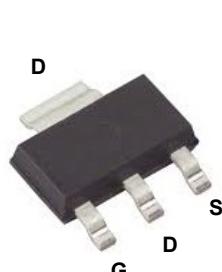
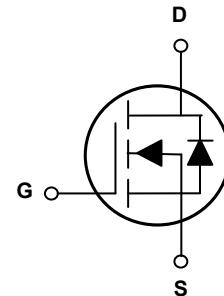


Main Product Characteristics

$V_{(BR)DSS}$	150V
$R_{DS(ON)}$	56mΩ (Typ.)
I_D	14A



SOT-223



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFL72015 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	V_{DS}	150	V
Gate-to-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current, @ Steady-State ($T_C=25^\circ\text{C}$) ¹	I_D	14	A
Continuous Drain Current, @ Steady-State ($T_C=100^\circ\text{C}$)		10	A
Pulsed Drain Current ²	I_{DM}	56	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	30	W
Linear Derating Factor ($T_C=25^\circ\text{C}$)		0.24	W/ $^\circ\text{C}$
Single Pulse Avalanche Energy ³	E_{AS}	25	mJ
Junction-to-Case	R_{eJC}	4.16	$^\circ\text{C}/\text{W}$
Junction-to-Ambient (PCB Mounted, Steady-State) ⁴	R_{eJA}	50	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_A=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	150	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=150\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
		$T_J=125^\circ\text{C}$	-	-	50	
Gate-to-Source Forward Leakage	I_{GSS}	$V_{\text{GS}}=20\text{V}$	-	-	100	nA
		$V_{\text{GS}}=-20\text{V}$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_D=5\text{A}$	-	56	72	$\text{m}\Omega$
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	2.1	3	3.9	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=75\text{V}, f=1\text{MHz}$	-	518	-	pF
Output Capacitance	C_{oss}		-	76	-	
Reverse Transfer Capacitance	C_{rss}		-	3.3	-	
Total Gate Charge	Q_g	$I_D=5\text{A}, V_{\text{DS}}=75\text{V}, V_{\text{GS}}=10\text{V}$	-	9.1	-	nC
Gate-to-Source Charge	Q_{gs}		-	3.5	-	
Gate-to-Drain ("Miller") Charge	Q_{gd}		-	1.8	-	
Turn-On Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=30\text{V}, I_D=5\text{A}, R_{\text{GEN}}=6\Omega$	-	7.3	-	nS
Rise Time	t_r		-	24	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	14	-	
Fall Time	t_f		-	22	-	
Gate Resistance	R_g	$f=1\text{MHz}$	-	2.5	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_s	MOSFET symbol showing the integral reverse p-n junction diode.	-	-	20	A
Pulsed Source Current (Body Diode)	I_{SM}		-	-	80	A
Diode Forward Voltage	V_{SD}	$I_s=2\text{A}, V_{\text{GS}}=0\text{V}$	-	1	1.2	V
Reverse Recovery Time	T_{rr}	$I_s=5\text{A}, V_{\text{GS}}=0\text{V}$ $di/dt=100\text{A}/\mu\text{s}$	-	52	-	μs
Reverse Recovery Charge	Q_{rr}		-	0.12	-	nC

Notes:

1. Pulse test: pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
2. Repetitive rating; pulse width limited by max. junction temperature.
3. $L=0.5\text{mH}, R_G=25\Omega, V_{\text{DD}}=50\text{V}, I_{\text{AS}}=10\text{A}, T_J=25^\circ\text{C}$.
4. Device mounted on FR-4 PCB, 1 inch x 0.85 inch x 0.062 inch.

Typical Electrical and Thermal Characteristic Curves

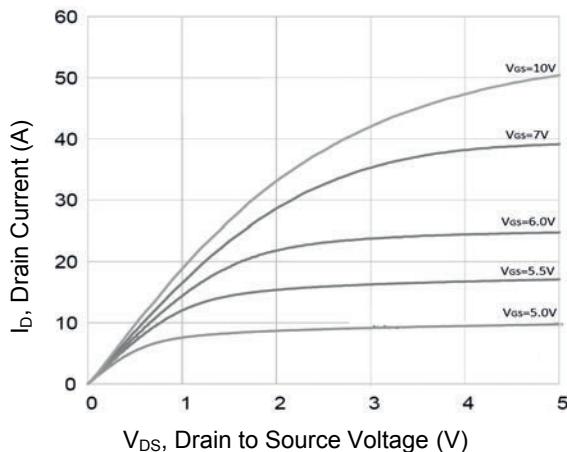


Figure 1. Typical Output Characteristics

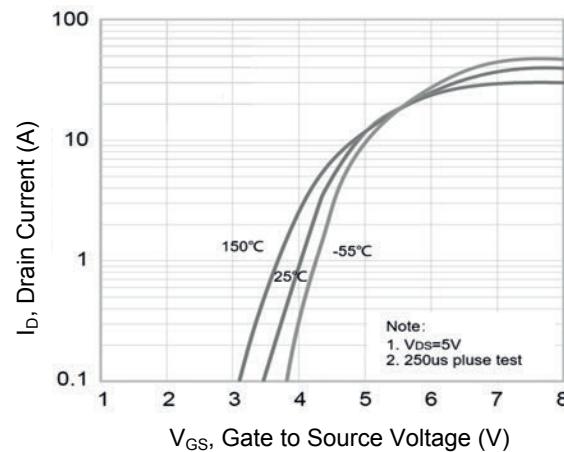


Figure 2. Transfer Characteristics

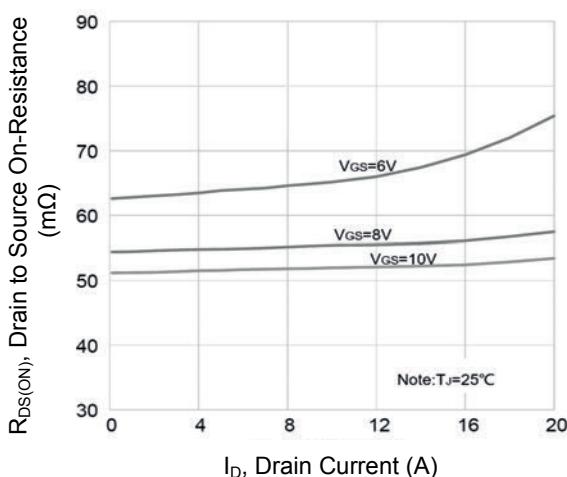


Figure 3. $R_{DS(ON)}$ vs. Drain Current

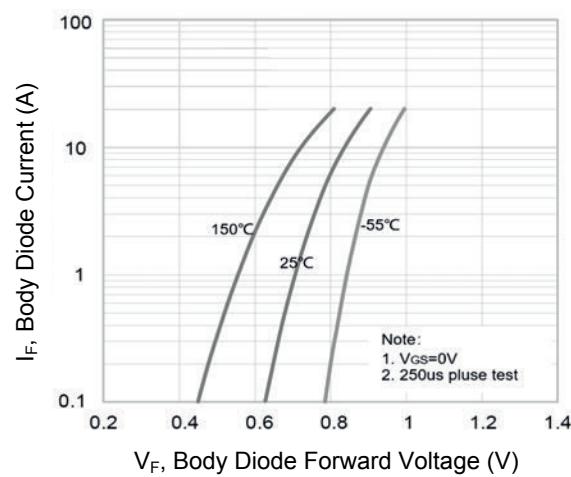


Figure 4. Body Diode Characteristics

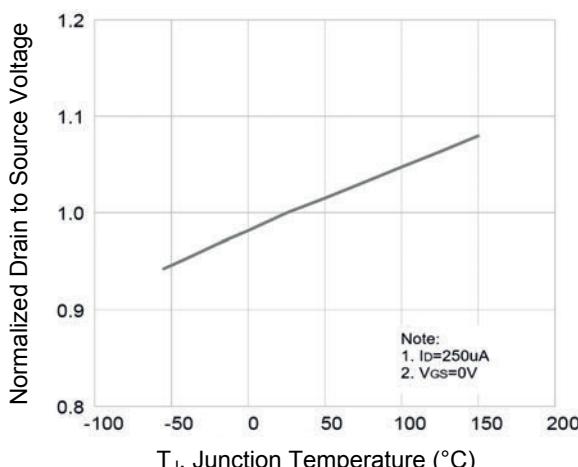


Figure 5. Normalized BV_{DSS} vs. T_J

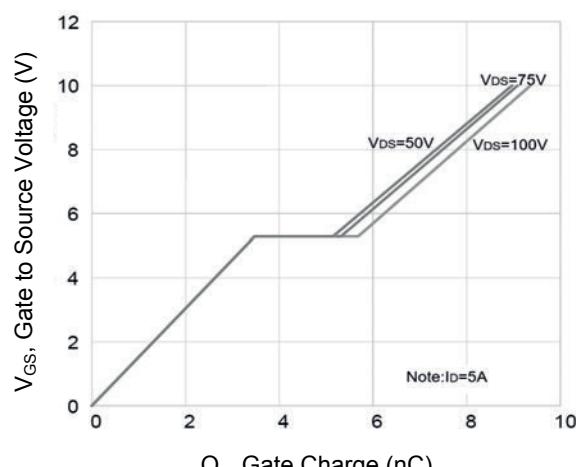


Figure 6. Gate Charge

Typical Electrical and Thermal Characteristic Curves

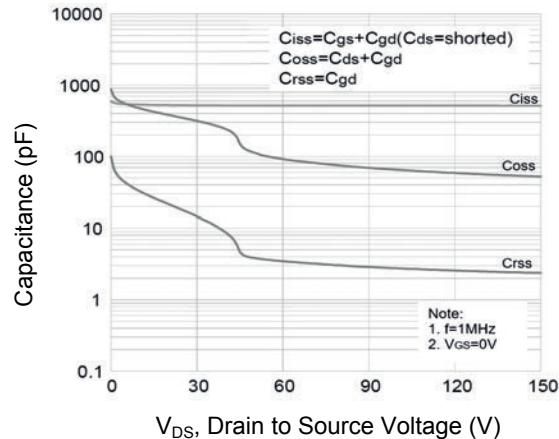


Figure 7. Capacitance Characteristics

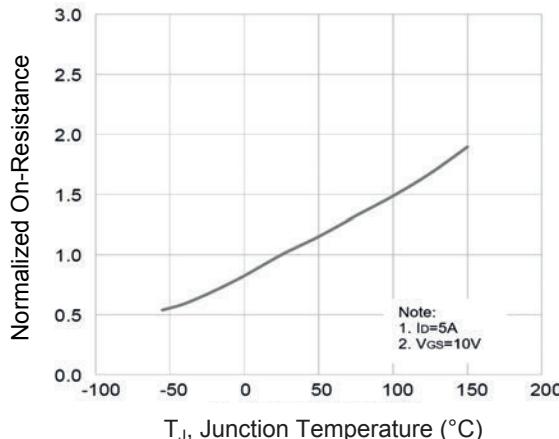
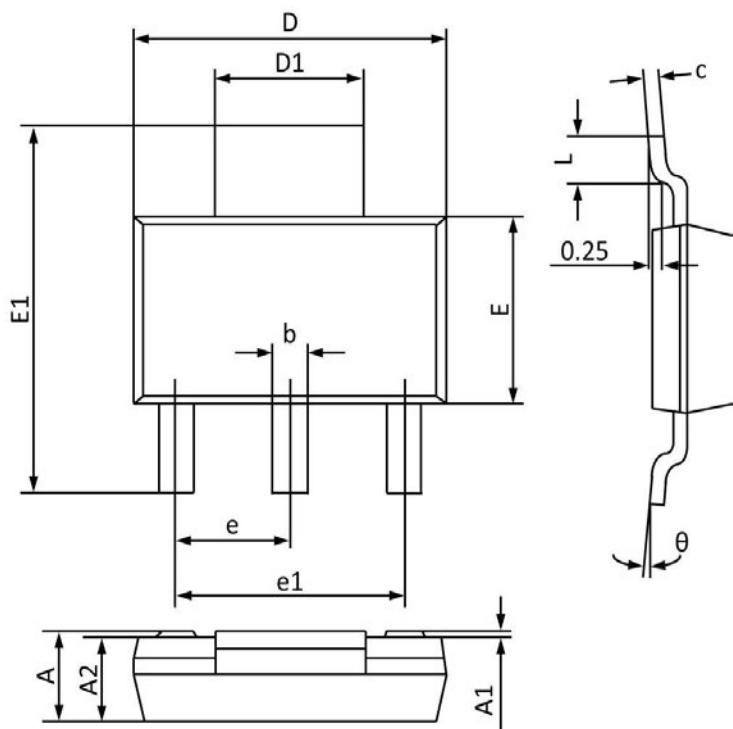


Figure 8. Normalized $R_{DS(ON)}$ vs. T_J

Package Outline Dimensions (SOT-223)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.50	1.80	0.060	0.071
A1	0.00	0.12	0.000	0.005
A2	1.45	1.75	0.057	0.069
b	0.60	0.82	0.024	0.032
c	0.20	0.35	0.008	0.014
D	6.20	6.70	0.244	0.264
D1	2.90	3.10	0.114	0.122
E	3.30	3.70	0.130	0.146
E1	6.70	7.30	0.264	0.287
e	2.30 BSC		0.091 BSC	
e1	4.40	4.70	0.173	0.185
L	0.90	1.15	0.035	0.045
θ	0°	10°	0°	10°